



**VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE**

(An ISO 9001 : 2000 Certified Institution)  
(Sponsored by Lavu Educational Society)



Deshmukhi

30<sup>th</sup> Aug 2016.

To,

The Head of the department,

Mechanical Engineering.

**Sub: Proposal to conduct a National level workshop on "Polymer composite materials"-Reg**

Respected sir,

We on behalf of our department would like to ask your permission to allow us to conduct a National level workshop on "Polymer composite materials"-for this academic year 2016-17. We do believe that this endeavor may hone us more in our teaching skills and research work. We would like to ask your assistance and recommendations for the best duration and venu for this upcoming plan. We welcome your intelligent suggestions regarding this matter. Hoping for your feedback with regards with this request .

Thanking sir,

N.Venkateshwarlu  
Assosiate professor



**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (Md),  
Yadadri Bhuvanagiri Dist-508 224.



**VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE**

(An ISO 9001 : 2000 Certified Institution)  
(Sponsored by Lavu Educational Society)



Department of Mechanical Engineering

Deshmukhi  
1st November, 2016.

To  
The principal  
Vignan institute of Technology and Science  
Deshmukhi,  
Nalgonda- 508284.

**Sub: Request for permission to conduct a national level workshop "Polymer Composite Materials" – Reg.**

Respected sir,

As decided in our departmental academic committee (DAC) meeting, we would like to conduct a national level workshop "Recent Advances in Composite Materials" on 14/11/2016-19/11/2016. In this regard, we request your authority to grant permission to do so. The budget details, program details are attached along with this letter for your reference. Kindly permit us to conduct seminar and sanction an amount of Rs. 1,45,000/- for conducting the above mentioned workshop.

Thanking sir,

Yours sincerely

**HOD-ME**  
**(Dr.K.Chandrashekar)**

OK,  
Sivaram

**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (Md),  
Yadadri Bhuvanagiri Dist.-508 284.





M. Ramji <ramji.m008@gmail.com>

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**A National level workshop on "Polymer Composite Materials" on 14th to 19th November, 2016 -chief Guest &Resource person-Invitations**

---

M. Ramji <ramji.m008@gmail.com>

Thu, Nov 3, 2016 at 1:13 PM

To: "MECHANICAL ENGG., DEPARTMENT, VITS -DESHMUKHI, HYD" <ncltme2011.vignan@gmail.com>

Dear sir,

I confirm my participation as a Chief Guest for the National level workshop on "Polymer Composite Materials" on 14<sup>th</sup> to 19<sup>th</sup> November, 2016.

Thanking you sir,

From

Dr. M. Ramji  
Professor, In Mechanical Engineering,  
IIT HYD

PRINCIPAL  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pachampally (MdI),  
Yadadri Bhuvanagiri Dist.-508 284.

[Quoted text hidden]





VITS -DESHMUKHI, HYDERABAD Vignan &lt;ncltme2011.vignan@gmail.com&gt;

**A National level workshop on "Polymer Composite Materials" on 14th to 19th November, 2016 -chief Guest &Resource person-Invitations**

1 message

MECHANICAL ENGG., DEPARTMENT, VITS -DESHMUKHI, HYD

Wed, Nov 2, 2016 at 2:19 PM

&lt;ncltme2011.vignan@gmail.com&gt;

To: M. Ramji &lt;ramji.m008@gmail.com&gt;

Department of Mechanical Engineering, Vignan Institute of Technology & Science, Deshmukhi, Nalgonda is organizing a two-day workshop on "Polymer Composite Materials" on 14<sup>th</sup> to 19<sup>th</sup> November 2016

In this connection, we are privileged to invite you as a Chief Guest for the inauguration of the above-said program in 14<sup>th</sup> Nov 2016. On this occasion, you are also requested to deliver a lecture in Fracture Mechanics of Composites, An overview of SiC-SiC composites & its applications & Nano composites.

The target group of the workshop is the faculty of various Engineering institutions. Your industrial expertise in this area will be in continuous contact with you for necessary arrangements and assistance.

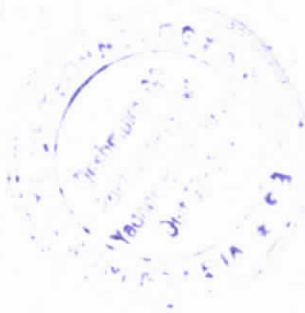
Your positive response in this regard will be recorded with great esteem.

Thanking you,

--  
HOD

MECHANICAL ENGG.,  
VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE  
Near Ramoji Film City,  
DESHMUKHI,  
Pochampally (Mandal),  
Yadadri Bhuvanagiri (District), Telangana State  
Hyderabad---- PIN CODE: 508 284

PRINCIPAL  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (Mdl)  
Yadadri Bhuvanagiri Dist -508 284





**VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE**

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Date: 4<sup>th</sup> Nov, 2016

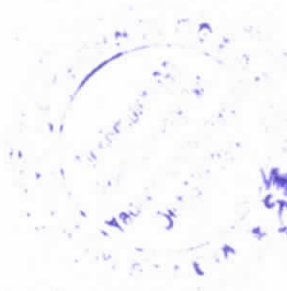
**CIRCULAR**

It is my pleasure to inform all the teaching and non teaching staff members of our department, that we are going to conduct a national level workshop on "Polymer Composite Materials" on 14<sup>th</sup> to 19<sup>th</sup> November, 2016. In this regard I request you all to participate actively in the workshop and make it a grand success.

HOD-MECH

PRINCIPAL

Vignan Institute of Technology and Science  
(VI) Beshmukhi, Pochampally (M.A.)  
Aravali Bhuvanagiri Dist.-508 284.



**About Institute:**

Vignan Institute of Technology & Science, established in 1999, is a fast growing technical institution in Andhra Pradesh under the auspices of the well known Vignan Group of Educational Institutions. VITS is offering five undergraduate programs and six post graduate programs includes MBA & MCA. Currently VITS is handling research projects worth of Rs.0.6 crore. Our student of VITS won gold medal offered by JNTUH for academic excellence. VITS has got in association with number of organizations including IIT-Hyderabad. The Vignan Group is synonymous with quality engineering education having its institutions across the length and breadth of Andhra Pradesh. VITS is situated near the world renowned Ramoji film city, in an Idyllic spot amidst a vast area of 350 acres with sylvian surroundings.

**Mission & Vision**

To provide high quality academic programmers, training activities, research facilities & opportunities supported by continuous industry - institute interaction aims at promoting employability, leadership, entrepreneurship & research aptitude among students and contribute to the economic well being and technological development of the Region, State and Nation.

**Organizing committee****Chief Patron**

Dr. L. Rathaiah, Chairman  
Vignan Group of Institutions

**Patron**

Mr. B.Shraavan, CEO  
Vignan Group of Institutions, Hyderabad

**Steering Committee**

Prof. G.V.Rao, Director, ASL (Retd.)  
Prof.N.Leela Prasad, Assoc.Professor  
VITS.

**Organizing Secretary**

Dr. K. Chandra Shekar, Assoc.Prof  
Department of Mechanical Engineering  
Mobile: (0) 9849505037

**Co-ordinator**

Mr. N.Venkateswarlu, Associate Professor  
Department of Mechanical Engineering

Email -id: [tedms.vits@gmail.com](mailto:tedms.vits@gmail.com)

**Organizing Committee Members**

- ❖ Mr. K. Mangapathi Rao 8106046056
- ❖ Mrs. M. Radhika 9666181228
- ❖ Mr.A.Naresh Kumar 9704577583

**Venue:-**

VITS sanskriti seminar hall

**Institute Location:**

VITS is located in the suburbs of Hyderabad city near to Ramoji Film City.

A National level Workshop  
On

**"POLYMER COMPOSITE MATERIALS"**

14<sup>th</sup> to 19<sup>th</sup> November, 2016

Organizing Secretary

**Dr. K. Chandra Shekar**

Head, Department of Mechanical Engineering

Co-ordinator

Mr. N.Venkateswarlu, Associate Professor

Department of Mechanical Engineering



Organized by

Department of Mechanical Engineering

**Vignan Institute of Technology and Science (VITS)**

Vignan Hills, Deshmukhi (V) - 508284

Near Ramoji Film City

HYDERABAD

Website: [www.vignanits.ac.in](http://www.vignanits.ac.in)

Ph.No: 9177701797/98

Email: [tedms.vits@gmail.com](mailto:tedms.vits@gmail.com)



**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (M),  
Yadadri Bhuvanagiri Dist.-508 284.

### **Introduction**

Composite materials have, by now, become very popular inputs to mechanical, aerospace industry (Thermo set composites are being specified for wings and other applications in commercial, civilian and military aerospace application). The civil industry is also recognizing the importance of their applications for energy saving and corrosion prevention. It is time to expose our teaching community, students and research scholars on the principles, constituent materials, manufacturing processes and quality assurance practices through this workshop. Eminent persons from academic and industry with vast experience will be addressing the participants. In addition, an interesting presentation by eminent professor *Dr. M.Ramji*, Professor, IIT-HYD., is planned during the inaugural session.

### **How to register?**

- Registration fee: Rs. 300/-
- Along with the registration form should reach the Coordinator latest by 14<sup>th</sup> to 19<sup>th</sup> November, 2016.
- Registration form is available on the college website: [www.vignanits.ac.in](http://www.vignanits.ac.in)
- Fill and send the registration form softcopy to the email-id: [tedms.vits@gmail.com](mailto:tedms.vits@gmail.com)

### **Certificate**

Participation certificates will be presented.

### **Resource Persons:**

- > Dr. M. Ramji  
Professor, IIT HYD
- > Dr.D.Linga raju  
Assoc.Professor,  
GVP College of Engineering,  
Visakhapatnam
- > Prof. G.V.Rao,  
Director, ASL (Retd.)

### **Contents of this Workshop**

The Contents of this workshop are:

- > Fracture Mechanics of Composites.
- > An over view of SiC-SiC composites & its applications & Nano composites.
- > Recent advances in Epoxy based composite materials.
- > Mechanical characterization of materials.
- > FE analysis of carbon fiber reinforced matrix.
- > Classical laminate theory of composites.
- > An over view of Bio-degradable composites.
- > Classical laminate theory of composites
- > An over view of hybrid composites

### **Who should attend?**

UG, PG students from various Engineering institutions & Faculty members.

**Vignan Institute of Technology and Science (VITS)**  
Deshmukhi-508284  
Near Ramoji Film City, HYDERABAD  
A National level Workshop

On

### **"POLYMER COMPOSITE MATERIALS"**

### **Registration Form**

1. Name :
2. Qualification :
3. Designation :
4. Department:
5. Institution/ Organization :
6. Address for Communication:
7. Contact Phone/Mobile-No:
8. Email :
9. Fee Details :

(Signature of the Applicant)



**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (MCH),  
Yadadri Bhuvanagiri Dist.-508 284.



### Introduction

Composite materials have, by now, become very popular inputs to mechanical, aerospace industry (Thermo set composites are being specified for wings and other applications in commercial, civilian and military aerospace application). The civil industry is also recognizing the importance of their applications for energy saving and corrosion prevention. It is time to expose our teaching community, students and research scholars on the principles, constituent materials, manufacturing processes and quality assurance practices through this workshop. Eminent persons from academic and industry with vast experience will be addressing the participants. In addition, an interesting presentation by eminent professor *Dr. M.Ranjit*, Professor, IIT-HYD., is planned during the inaugural session.

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### Certificate

Participation certificates will be presented.

### Resource Persons:

- Dr. M. Ranjith Professor, IIT-HYD
- Dr. D. Linga Raju Assoc. Professor, GVP College of Engineering, Visakhapatnam
- Prof. G. V. Rao, Director, ASL (Retd.)

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UG, PG students from various Engineering institutions & Faculty members.

Vignan Institute of Technology and Science (VITS)  
Deshmukhi-508284

Near Ramoji Film City, HYDERABAD  
A National level Workshop

On

## "POLYMER COMPOSITE MATERIALS"

### Registration Form

1. Name : M. Abhinav
2. Qualification : M.Tech
3. Designation : Asst. Professor
4. Department : Mech Dept
5. Institution/Organization : VITS, Deshmukhi
6. Address for Communication:  
Vign Hills, Deshmukhi (Vil),  
Pachampally (M.H.), Nalgonda - 508284
7. Contact Phone/Mobile No: 9985128676
8. Email : [abhinav200@gmail.com](mailto:abhinav200@gmail.com)
9. Fee Details :

(Signature of the Applicant)

*Shukuma*  
PRINCIPAL

Vignan Institute of Technology and Science  
(V) Deshmukhi, Pachampally (M.H.)  
Nalgonda, Dist: Nalgonda-508284

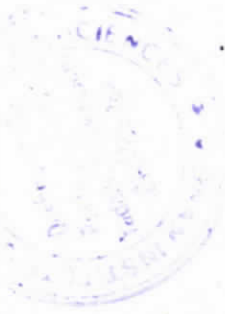


DAY 1	DAY 2	DAY 3	DAY 4	DAY 5	DAY 6
21/06/2019	22/06/2019	23/06/2019	24/06/2019	25/06/2019	26/06/2019
Dr. R. Chandra Sekhara	Dr. S. Deves Prasad	Dr. Krishna Mohan Raju	Dr. S.M Hussaini	Dr. B. Singaravel	Dr. B. Nageswara rao
Dr. N. Leela Prasad	Mr. N. Venkateshwarlu	Mr. G. Narendar	Mr. K. Mangapathi Rao	Mrs. M. Radhika	Mr. A. Naresh Kumar
Mrs. Ch. Divya	Ms. P. Madhavi	Mr. P. Vinod	Mr. V. Guru Shanker	Mr. M. Abhinav	Mr. Suresh vadepu
Mr. N. Chandra Sekhara Reddy					

*Sankumar*

PRINCIPAL

Vignan Institute of Technology and Science  
(Vi) Deshmukhi, Pechampally (Midi)  
Yadadri Bhuvanagiri Dist., 508 002



24	Mr. R. Eshwaraiah	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
25	Mr. M. Ashok	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
26	Mr. G. Srikar	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
27	Mr. T. Aravind	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
28	Mrs. Y. Lakshmi Prasanna	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
29	Mr. G. Laxmi Srinivas	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
30	Mr. S. Veerendra Prasad	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
31	Mr. Deepak suna	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
32	Mr. Avadh Kishore Prasad	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
33	Mr. M. Balasouri	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
34	Mr. D. Rajesh	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
35	Mr. D. Prabhu Shankar	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
36	Mr. K. Ravinder Reddy	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
37	Mr. D. Srinivas	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
38	Mr. R. Ranjith kumar	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
39	Mr. Anup kumar jana	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>	<i>[Signature]</i>
40								
41								



*[Signature]*  
PRINCIPAL

Vignani Institute of Technology and Sciences  
(V) Dinsankh, Dinsankh, Dinsankh, Dinsankh



	cash	cash	cash	cash	cash	cash	cash
46	Dr. Dinash	Dr. Dinash	Dr. Dinash	Dr. Dinash	Dr. Dinash	Dr. Dinash	Dr. Dinash
47	CH. Navaneeth	CH. Navaneeth	CH. Navaneeth	CH. Navaneeth	CH. Navaneeth	CH. Navaneeth	CH. Navaneeth
48	G. Srinivas Kumar	G. Srinivas Kumar	G. Srinivas Kumar	G. Srinivas Kumar	G. Srinivas Kumar	G. Srinivas Kumar	G. Srinivas Kumar
49	G. Mani Kantam Reddy	G. Mani Kantam Reddy	G. Mani Kantam Reddy	G. Mani Kantam Reddy	G. Mani Kantam Reddy	G. Mani Kantam Reddy	G. Mani Kantam Reddy
50	Rehal Khanna	Rehal Khanna	Rehal Khanna	Rehal Khanna	Rehal Khanna	Rehal Khanna	Rehal Khanna
51	P. Imran Khan	P. Imran Khan	P. Imran Khan	P. Imran Khan	P. Imran Khan	P. Imran Khan	P. Imran Khan
52	P. Sai Kumar	P. Sai Kumar	P. Sai Kumar	P. Sai Kumar	P. Sai Kumar	P. Sai Kumar	P. Sai Kumar
53	P. Anitha Kumal	P. Anitha Kumal	P. Anitha Kumal	P. Anitha Kumal	P. Anitha Kumal	P. Anitha Kumal	P. Anitha Kumal
54	N. Sai Nichil	N. Sai Nichil	N. Sai Nichil	N. Sai Nichil	N. Sai Nichil	N. Sai Nichil	N. Sai Nichil
55	N. Rajesh	N. Rajesh	N. Rajesh	N. Rajesh	N. Rajesh	N. Rajesh	N. Rajesh
56	Pragya	Pragya	Pragya	Pragya	Pragya	Pragya	Pragya
57	P. Saivardhan Reddy	P. Saivardhan Reddy	P. Saivardhan Reddy	P. Saivardhan Reddy	P. Saivardhan Reddy	P. Saivardhan Reddy	P. Saivardhan Reddy
58	P. Vishal	P. Vishal	P. Vishal	P. Vishal	P. Vishal	P. Vishal	P. Vishal
59	P. Leela Gowtham	P. Leela Gowtham	P. Leela Gowtham	P. Leela Gowtham	P. Leela Gowtham	P. Leela Gowtham	P. Leela Gowtham
60							

*Sobhanas*

PRINCIPAL  
Vignan Institute of Technology and Science  
(M) Deshmukhi, Pochampally (Mdt),  
Yadadri Bhuvanagiri Dist. - 508 204



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji film city, Deshmukhi (v), Nalgonda Dist-508 284

## *Certificate of Participation*

This is to certify that Dr. /Mr. /Ms. .... A.NARESH KUMAR ..... of..... VITS-DESHMUKHI .....

Participated in "Polymer Composite Materials" a national level workshop, organized by  
Department of Mechanical Engineering, vignan institute of technology & Science from 14<sup>th</sup> to  
19<sup>th</sup> November 2016.

N. Venkateshwarlu  
Coordinator

Dr. K. Chandra shekar  
Head of the Department

Dr. G. Durga sukumar  
Principal



PRINCIPAL  
Vignan Institute of Technology and Science  
(M) Deshmukhi, Pochampally (Mall),  
Nalgonda, Andhra Pradesh



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji film city, Deshmukhi (v), Nalgonda Dist-508 284

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This is to certify that Dr. /Mr. /Ms. .... of VITS-DESHMUKHI

B.SINGARAVEL

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N.Venkateshwarlu  
Coordinator

Dr.K.Chandra shekar  
Head of the Department

Dr.G.Durga sukumar  
Principal

Vignani-Institute of Technology and Science  
(VI) Deshmukhi, Pechampally (Mdy),  
Yadadri Bhuvanagiri Dist.-508 284.





## VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji film city, Deshmukhi (v), Nalgonda Dist-508 284

### *Certificate of Participation*

This is to certify that Dr. /Mr. /Ms. .... D.PRABHU SILANKAR ..... of..... VITS-DESHMUKHI.....

Participated in "Polymer Composite Materials" a national level workshop, organized by  
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N. Venkateshwarlu  
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Head of the Department

Dr.G.Durga sukumar  
Principal

*G. Durga Sukumar*  
PRINCIPAL  
Vignan Institute of Technology and Science  
(V) Deshmukhi, Nalgonda, Dist-508 284



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE  
Vignan Hill, Deshmukhi (Village), Pochampally (Mandal), Nalgonda (Dist)-508284

Department of Mechanical Engineering

*A National level Workshop On*  
**"POLYMER COMPOSITE MATERIALS"**

14<sup>th</sup> to 19<sup>th</sup> November, 2016

Feedback format

Dear participant,

We are delighted to have you as a participant for this workshop. Your valuable feedback is of improve help to make possible improvements in conducting such workshops

A. Please rate in a scale of 1 to 4 for the following:

4-Very much useful 3-Useful 2-Useful to some extent 1-Not useful

1. Adequacy of course content {3}
2. Usefulness of workshop {3}
3. Effectiveness of Lecturers {4}

S.No	Name of Resource person	Topic	Rating
1	Dr. M. Ramji	Fracture Mechanics of Composites. An over view of SiC-SiC composites & its applications & Nano composites.	4
2	Dr.D.Linga raju	Recent advances in Epoxy based composite materials	3
3	Prof. G.V.Rao,	Mechanical characterization of materials	4
4	Dr.K.Chandra Shekar	FE analysis of carbon fiber reinforced matrix.	3
5	Dr.M.Sridhar Babu	Classical laminate theory of composites An over view of Bio-degradable composites.	4

B. Please rate in a scale of 1 to 4 for the following:

4-Very Good 3-Good 2-Aveage 1-poor

1. Arrangements at Hall (OHP, LED'S)
2. Hospitality
3. Lunch & Other arrangements

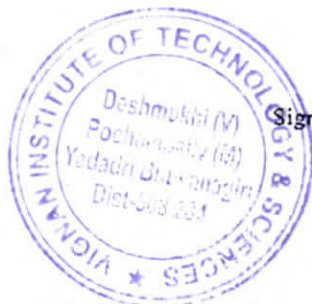
{3}  
{4}  
{4}

*[Handwritten Signature]*

C. Do you want to have such workshops in future? Yes/No

D. Any Suggestions (use rear side if necessary)

**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (Midi),  
Yadadri Bhuvanagiri Dist.-508 284.



*[Handwritten Signature]*  
Signature of the Participant



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE  
Vignan Hill, Deshmukhi (Village), Pochampally (Mandal), Nalgonda (Dist)-508284

**Department of Mechanical Engineering**

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2. Usefulness of workshop {4}
3. Effectiveness of Lecturers {3}

S.No	Name of Resource person	Topic	Rating
1	Dr. M. Ramji	Fracture Mechanics of Composites. An over view of SiC-SiC composites & its applications & Nano composites.	4
2	Dr.D.Linga raju	Recent advances in Epoxy based composite materials	3
3	Prof. G.V.Rao,	Mechanical characterization of materials	3
4	Dr.K.Chandra Shekar	FE analysis of carbon fiber reinforced matrix.	4
5	Dr.M.Sridhar Babu	Classical laminate theory of composites An over view of Bio-degradable composites.	2

B. Please rate in a scale of 1to 4 for the following:

4-Very Good 3-Good 2-Aveage 1-poor

1. Arrangements at Hall (OHP, LED'S)
2. Hospitality
3. Lunch &Other arrangements

{3}  
{3}  
{3}

C. Do you want to have such workshops in future? Yes/No

D. Any Suggestions (use rear side if necessary)



{3}

{3}

{3}

**PRINCIPAL**

Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (Mdl),  
Yadadri Bhuvanagiri Dist.-508 284.

Signature of the Participant







VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE  
Vignan Hill, Deshmukhi (Village), Pochampally (Mandal), Nalgonda (Dist)-508284

Department of Mechanical Engineering

*A National level Workshop On*  
**"POLYMER COMPOSITE MATERIALS"**

14<sup>th</sup> to 19<sup>th</sup> November, 2016

Feedback form

Dear participant,

We are delighted to have you as a participant for this workshop. Your valuable feedback is of improve help to make possible improvements in conducting such workshops

-A. Please rate in a scale of 1to4 for the following:

4-Very much useful 3-Useful 2-Useful to some extent 1-Not useful

- |                               |     |
|-------------------------------|-----|
| 1. Adequacy of course content | {3} |
| 2. Usefulness of workshop     | {4} |
| 3. Effectiveness of Lecturers | {3} |

S.No	Name of Resource person	Topic	Rating
1	Dr. M. Ramji	Fracture Mechanics of Composites. An over view of SiC-SiC composites & its applications & Nano composites.	4
2	Dr.D.Linga raju	Recent advances in Epoxy based composite materials	4
3	Prof. G.V.Rao,	Mechanical characterization of materials	3
4	Dr.K.Chandra Shekar	FE analysis of carbon fiber reinforced matrix.	2
5	Dr.M.Sridhar Babu	Classical laminate theory of composites An over view of Bio-degradable composites.	4

B. Please rate in a scale of 1 to 4 for the following:

4-Very Good 3-Good 2-Average 1-poor

1. Arrangements at Hall (OHP, LED'S)
2. Hospitality
3. Lunch & Other arrangements

{3}  
{3}  
{4}

*S. Subramanian*

PRINCIPAL

Vignan Institute of Technology and Science  
(Vi) Deshmukhi, Pochampally (Mdl),  
Mandali Bhumana, Dist.-508 284.

C. Do you want to have such workshops in future? Yes/No

D. Any Suggestions (use rear side if necessary)

*P. Sankar*  
Signature of the Participant





VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

(An ISO 9001 : 2000 Certified Institution)  
(Sponsored by Lavu Educational Society)



Department of Mechanical Engineering

Deshmukhi  
21st November, 2016.

To,  
The Principal,  
Vignan Institute of Technology & Science,  
Deshmukhi.

**Sub: Expenditure for the national level workshop on "Polymer Composite Materials"**

Dear Sir,

As a part of conducting workshop for teaching staff for this Academic year we've conducted a one week Program on "Polymer Composite Materials" from 14/11/2016-19/11/2016, successfully. As the expected budget was Rs.1,45,000/-,

Thanking you sir

Yours sincerely

Head of the department

Dr.K.Chandra shekar

**Expenditure Incurred Amount for Polymer Composite Materials:**

1. Resource person Honorarium: 90,000/-
2. Transport for Resource person: 15,000/-
3. Flexi: 2,000/-
4. Food and Snacks: 25,000/-
5. Stationary: 3000/-
6. Memento: 10,000/-

---

Total: 1,45,000/-

**PRINCIPAL**

Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (Midi),  
Yadadri Bhuvanagiri Dist.-508 284.





**VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE**

(An ISO 9001 : 2000 Certified Institution)  
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SUMMARY OF EVENTS

A National level Workshop on

**POLYMER COMPOSITE MATERIALS**

Date of Event Conducted: 14th to 19th November, 2016.

Organizer: Dr. K. Chandra Shekar

Head, Department of Mechanical Engineering

Coordinator:

1. N.Venkateshwarlu, Associate Professor

NO. Of faculty Participated: 45

Name of Resource Persons & Designation:

- Dr. M. Ramji  
Professor, IIT HYD
- Dr.D.Linga raju  
Assoc.Professor,  
GVP College of Engineering.  
Visakhapatnam
- Prof. G.V.Rao,  
Director, ASL (Retd.)

The program schedule:

Date	Schedule
14/11/2016	Registration & Inauguration
	Fracture Mechanics of Composites. An over view of SiC-SiC composites & its applications
15/11/2016	Nano composites Recent advances in Epoxy based composite materials



*S. Subramanian*

**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (M.H.),  
Yadadri Bhuvanagiri Dist.-508 284.



**VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE**

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi

EAMCET CODE : VGNT



Date: 20/01/2017

To  
The Principal, VITS.

Sub: Conducting a workshop in ECE department.

Respected Sir,

MHRD is going to conduct a Two week ISTE workshop on “**CMOS mixed signal and Radio frequency VLSI Design**” in our college to enhance the skills in teaching staff. This is organized by Professors from IIT Kharagpur, where they will be trained on basic operations in RF circuit design and VLSI.

This will be a Two weeks workshop from 30<sup>th</sup> Jan- 4<sup>th</sup> Feb, 2017

With Best Regards,

  
HoD ECE



  
PRINCIPAL  
Vignan Institute of Technology and Science  
Deshmukhi, Bhongir Yadadri Dist-508 284



# VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.  
Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi

EAMCET CODE : VQNT
































## CIRCULAR

25<sup>th</sup> January 2017

All the Teaching staff are here by informed that 30<sup>th</sup> Jan- 4<sup>th</sup> Feb,2017, MHRD is conducting a Two-week ISTE workshop on "CMOS mixed signal and Radio frequency VLSI Design" by Prof. T. K. Bhattacharya, Prof. Indrajit Chakrabarti, and Dr. Mrigank Sharad.

  
HOD-ECE

cc: Principal for kind information,  
All ECE Staff.  
Transport Department

S.No	Name of the staff	Signature	S.N	Name of the staff	Signature
1	Dr.Nali Dinesh Kumar		23	Mr. B. Ravi Kumar	
2	Dr.Palagiri Anjani Harsha Vardhini		24	Mr. Ch. Vamshi Krishna	
3	Dr.Vijayalaxmi Biradar		25	Mr. S. Praveen	
4	Dr.Jillella Venkateswara Rao		26	Ms. M. Kavitha Rani	
5	Mr. Vijaykumar Urkude		27	Ms. Maddi Shireesha	
6	Mrs.Nelapati Koteswaramma		28	Ms. Y. Jyothi	
7	Mrs.Pydimarri Padmaja		29	Ms.D.Swarupa Rani	
8	Mr.Nageswara Rao P		30	G.Upendar	
9	Mr. Veguru Prakasam				
10	Mr.Patri Upender				
11	Mr. Sandeep Pingili				
12	Mr.B.Madhusudhan Reddy				
13	Mr.Vikas Kumar Tiwari				
14	Mr. D. Srinivas				
15	D Sandeep				
16	D.Bindu Tushara				
17	K.Murali Chandara Babu				
18	Mr. N. Pavan Kumar				
19	Appidi Kavitha Reddy				
20	Mr. P. Srinivasu				
21	Mr. Vuyyala Shankar				
22	Mrs.R.B.Kalyani				

Coordinator

  
Mr. Vijaykumar Urkude



  
PRINCIPAL

Vignan Institute of Technology and Science  
Deshmukhi, Bhongir Yadadri Dist-508 284

#### Eligibility:

- 1) He/ she must be a faculty member in the Department of Electronics and Communication Engineering / Electrical Engineering / Computer Science and Engineering / Instrumentation Engineering / Information Technology / Physics with basic VLSI knowledge/ Mathematics with basic VLSI knowledge/ Material Science with basic VLSI knowledge/ related Departments and Centres.
- 2) B.E. / B. Tech. or equivalent degree holders in the above mentioned disciplines with minimum teaching experience of 2 years and moderate teaching experience in the area of Analog and Digital Circuits.
- 3) M.E / M.Tech. degree holders in the above mentioned disciplines with minimum teaching experience of 1 year and moderate teaching experience in the area of Analog and Digital Circuits.
- 4) Ph.D. degree holders should have a minimum teaching experience of 1 year and moderate teaching experience in the area of Analog and Digital Circuits.

#### Who may benefit:

The workshop is likely to benefit regular/visiting faculty colleagues who are teaching subjects like VLSI Design, Fundamentals of Radio Frequency Engineering, Design and Implementation of Mixed Signal Circuits and Systems, Design and Analysis of Radio Frequency Integrated Circuits, Mixed Signal and RFIC Design, Analog Front-End Design, Digital VLSI Circuits, VLSI Architecture for Video Processing, etc.

#### Note:

Please note that this ISTE STTP is conducted under the CEP IIT Kharagpur. Live recording of the course and other created contents will be released under Open Source through a portal. The recorded CD/DVD of the course lectures will be available for distribution, at cost, to any individual or institution. All participants are required to sign an undertaking for such release of contents contributed by them during and after the STTP. The recognition and citation will naturally be made for all contributors.

#### Course Fee:

This ISTE STTP on 'CMOS, Mixed Signal and Radio Frequency VLSI Design' is funded by the National Mission on Education through ICT (MHRD, Government of India), therefore there is no course fee for participation.

#### Accommodation:

Remote Centers are being funded to provide tea/lunch on each day of the workshop, and for accommodation, wherever available, for a limited number of outstation participants. Travel expenses up to Rs. 1000/- one way and one-time will be reimbursed against proof of actual expenditure, for participants beyond a distances of 100 km from the remote center.

#### How to Apply:

Those willing to attend this course should register online.

#### Online Registration link:

<http://www.nmeict.iitkgp.ernet.in/vlsidmain.php>

#### Address for Communication:

Admin Team,  
Project "T10KT", IIT Kharagpur  
Vikramshila Building, ground floor of Kalidas Auditorium  
Kharagpur - 721302, West Bengal, India.

#### Contact Numbers:

Admin Team: +91 3222-281497  
Account Team: +91 3222-281498  
Moodle Team: +91 3222-281070  
A-View Team: +91 3222-281072  
Mobile Number: +91 8145226903

email: [office.nmeict@iitkgp.ac.in](mailto:office.nmeict@iitkgp.ac.in)

Website: <http://www.nmeict.iitkgp.ernet.in>

## TWO-WEEK ISTE STTP ON CMOS, MIXED SIGNAL, AND RADIO FREQUENCY VLSI DESIGN

National Mission on Education through ICT  
(MHRD, Govt. of India)

30<sup>th</sup> January, 2017 to 4<sup>th</sup> February, 2017



Indian Institute of Technology  
Kharagpur 721302  
India



PRINCIPAL  
Indian Institute of Technology and Science  
Bismillah, Bargarh Taluka, Dist-751 034



*Sukanya*

PRINCIPAL

Institute of Technology and Science  
Dabhanga, Bhandara Dist-508 284

### Introduction:

IIT Kharagpur and IIT Bombay are working together with Engineering Colleges of India to enhance the teaching skills of our faculty colleagues in core Engineering and Science subjects by conducting ISTE Short Term Training Programmes (STTPs) under Train Ten Thousand Teachers (T10KT) project using 353 established remote centers across India. Participating teachers attend live lectures at a remote center close to their own college, and also attend tutorial and lab sessions conducted in the same centers. The lecture transmission and live interaction takes place in distance mode using A-VIEW technology through Internet at the selected remote centers across the country. Since December 2009, a number of two-week ISTE STTPs were conducted on various Engineering subjects. We have reached out to more than 1,00,000 teachers and helped them to enhance their teaching skills in these subjects.

In order to run these STTP at selected remote centers, we invite expert faculty members from various remote centers to a five-day Coordinators' training programme held at IIT Kharagpur or at IIT Bombay, at least two months before the main STTP. The trained Coordinators then act as Workshop Coordinators during the main STTP liaising between the participants at their Remote Centers and IIT Kharagpur / IIT Bombay from where the interactive lectures are transmitted live. During the main STTP, the workshop Coordinator at every center supervises the tutorials and laboratories. All the lectures and tutorial sessions are recorded at IIT Kharagpur or at IIT Bombay. The final edited audio-visual contents, along with other course material are released under Open Source. The contents can be freely used later by all teachers, students and other learners.

In the backdrop of the success of these STTPs, we now announce another 6 day ISTE STTP on "CMOS, Mixed Signal and Radio Frequency VLSI Design" during January 30 - February 4, 2017 under Blended MOOCs (Massive Open Online Courses) model.

Here,

1. The participating teachers will complete the equivalent of two-week full time work online, spread over 6 physical weeks where video lectures and assignments will be uploaded beforehand.
2. After completing the online assignments spread over 4 to 5 weeks the participants will assemble at the selected Remote Centers for 6 days face to face interaction and lecture sessions through A-VIEW and will complete team assignments, tutorials, quizzes etc.

3. Offline assignments will be uploaded and the participants will have to complete these assignments within a stipulated time.
4. There will also be a system of students' feedback in the Main STTP.

The above proposed model is tentative and subjected to minor changes

### Course Justification:

Design, analysis and implementation of CMOS Mixed Signal and RFIC are attractive topics both in academia and industry. CMOS RFIC design is a challenging and critical engineering task particularly in the arena of deep sub-micron technology. There is a strong demand of manpower having thorough knowledge and hands on experiences in VLSI design using state of the art EDA tools. The course will focus on those issues along with case studies.

### Course Domain:

Electronics Engineering/Electrical Engineering/Computer Science and Engineering

### Expectation from the Participants:

It is assumed that before taking the courses, the participants are familiar with introduction to electronic circuit components, signal and system theory, VLSI design technology and basic CAD flow for electronic system.

### Course Objective:

Design and implementation methodology of:

1. Analog front end for sensor interface: Instrumentation Amplifier (IA), chopper, Variable Gain Amplifier (VGA)
2. Data converters: SAR & pipelined ADC.
3. Digital VLSI: a) Static MOS gate circuits b) High-Speed CMOS Logic Design c) Transfer Gate and Dynamic Logic Design d) VLSI architectures for video processing
4. RF circuits: Introduction to RFIC design.

5. Design and implementation of typical RF Tx-Rx sub-components: (a) Low Noise Amplifier (LNA) (b) Mixer (c) Frequency synthesizer (d) Power amplifier (e) Lay out and chip level integration

### Design Methodology:

From pen-paper design of various mixed signal/RF circuit blocks to schematic level implementation and then sub-sequent physical design, post-layout synthesis and simulation.

### Course Modules:

#### 1. Radio Frequency Integrated Circuits (RFIC):

**Module-A** Fundamental of RFIC design, How RF design differs from traditional analog design. Various commercial protocols, basic Tx-Rx Architecture.

**Module-B** Various performance parameters, Noise.

**Module C** Design of LNA, VCO, Mixer and frequency synthesizer, Power amplifier, High speed I/O(SERDES), layout of RF circuit design.

#### 2. Mixed Signal Blocks:

i) Digital VLSI Design: High-Speed CMOS Logic, Transfer Gate and Dynamic Logic

a) Basics of video processing

b) VLSI Architectures for video processing

ii) Analog Design: Analog front-end electronics (AFE), SAR ADC, Pipelined ADC.

### Duration and Venue:

The duration of the STTP will be six working days. It will start on Monday 30<sup>th</sup> January, 2017 at 9:30 AM and will end on Saturday 4<sup>th</sup> February, 2017. The participants must report to the respective remote centres by 8:00 AM on 30<sup>th</sup> January, 2017.

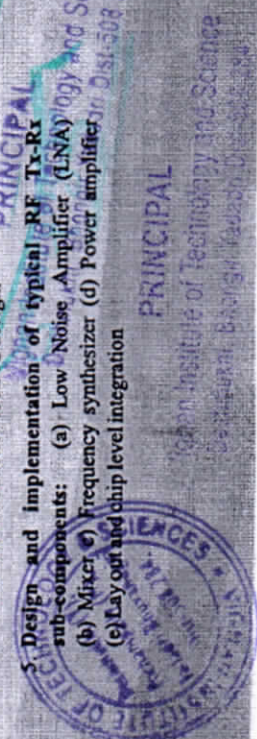
**Venue:** 206 remote centers located in different parts of the country. The list of participating remote centers is given along with online application form.

### Teaching Faculty:

1. Prof. T. K. Bhattacharya, Department of Electronics & Electrical Communication Engineering, IIT Kharagpur, Email: tkb@ece.iitkgp.ernet.in

2. Prof. Indrajit Chakrabarti, Department of Electronics & Electrical Communication Engineering, IIT Kharagpur, Email: indrajit@ece.iitkgp.ernet.in

3. Dr. Mrigank Sharad, Department of Electronics & Electrical Communication Engineering, IIT Kharagpur, Email: mrigank@ece.iitkgp.ernet.in





**BUDGET UTILIZATION GUIDELINES FOR TWO-WEEK ISTE STTP ON CMOS, MIXED SIGNAL AND RADIO FREQUENCY VLSI DESIGN**

**Requirement of Bank details:**

The Institutes which are already our regular remote centers, and have thus provided their bank details, need not submit these details again, unless there are any changes.

The new remote centers will have to upload their bank details on the following link for the main workshop on Design on Algorithms: <http://www.iitkgp.ernet.in/login>

Please note that the bank account has to be an official bank account of the Institute. The bank details provided by your Institute will be accepted without any responsibility by IIT Kharagpur.

**Fund transfers:**

We will start the fund transfers after the registration is closed.

We will post the bank transfer details such as the amount, date of credit and bank transaction number, on the above web portal, against each RC id. The access to this field will be restricted to the RCC (RC Coordinator) and the Institute Head of each RC.

The RCC and the Institute head will receive an auto generated mail from IIT Kharagpur with the bank transaction details and the settlement formats. The RCs will have to check the fund transfers in their bank account and acknowledge receipt on the above website.

**Budget (enclosed):**

The funds should be utilized strictly as per the sanctioned budget heads and the utilization guidelines. Inter head fund adjustments are not permitted.

**Budget heads under Fixed Cost:**

**1. Honorarium.**

Honorarium should be paid only under this head of the budget and only to the workshop team members listed under fixed cost.

The honorarium should be paid by cheques/bank transfers only from the same account in which IIT Kharagpur has transferred funds. Honorarium payments by cash are not allowed.

Honorarium budget for "a and b" will be clubbed to Rs 15000 in case the jobs of RCC and WSC are done by one person.

Honorarium figures shown under "c to f" are not person. The lump sum amount shown in each box has to be shared among the number of required team members. The payment should be made on the basis of the actual number of participants and not on the basis of registered number.

The required number of Teaching Assistants will be one per fifteen participants.



  
PRINCIPAL  
Vignan Institute of Technology and Science  
Deshmukhi, Bhongir Yadadi Dist. 508 284

## 2. Miscellaneous Expense Budget.

The Miscellaneous Budget can be used for following expenses:

- ✓ Registration Kits (comprising of a bag or a folder, pen, note book etc) The cost per kit should not exceed @ Rs150 per participant.
- ✓ Banners
- ✓ Stationery
- ✓ Postage
- ✓ Photocopy charges
- ✓ CA Fees (should not exceed Rs 1500)

Equipment/ Devices/Components/ Software/Gift Items/Memento Purchases, Internet, Photographer Charges, Food items, Petrol/Conveyance Bills and Honorarium payments are not allowed from Miscellaneous Head.

3. **Institute Expenses:** This amount has to be transferred to Institute Account for the common facilities provided by the Institute for the workshop.

### Budget heads under Variable Cost

1. **Food:** Remote centres should organize lunch and tea, biscuits for the participants and the workshop team members. The number of team members should not exceed 10 per day for lunch. The food order has to be reduced from the 2<sup>nd</sup> day onwards if the expected capacity is reduced. Food order must be given on lowest quotation basis. No reimbursements should be paid to the participants/team members for food.
2. **Accommodation (optional):** RCs may arrange accommodation for the outstation participants as per the availability. The excess expenditure, if any, has to be borne by the RC/participant.
3. **Travel reimbursement** should be paid to the outstation participants only. The distance between the participant's Institute and the RC has to be more than 100 km one way. DA should not be paid.

Please send your fund related queries to [mhrdeitaccts@gmail.com](mailto:mhrdeitaccts@gmail.com) or call +913222 281498.



*S. Subramanian*  
PRINCIPAL

Vignyan Institute of Technology and Science  
Deshmukhi, Bhojir Yadadri Dist-506 284

## Time Table for Two-Week ISTE STTP CMOS Mixed Signal and Radio Frequency VLSI Design 30th January to 04th February 2017

Date	Days	Morning			Lunch Break 12.45 PM to 1.45 PM	Afternoon		
		Inauguration 9.00 AM to 9.30 AM	Introductory Remark (TKB, IC, MS) 9.30 AM to 11.00 AM	11.00 AM to 11.15 AM Tea break		Basic Analog Design Part-I (MS) 11.15 AM to 12.45 PM	Basic Analog Design Part-II (MS) 1.45 PM to 3.15 PM	3.15 PM to 3.30 PM Tea Break
Jan 30, 2017	Monday							Specifications for Analog Fractional and Discussion (MS) 3.30 PM to 5.00 PM
Jan 31, 2017	Tuesday		Basic Digital VLSI Design Part-I (IC) 9.30 AM to 11:00 AM			Low Power Signal Processing Circuits (IC) 1.45 PM to 3.15 PM		Frontend Amplifier Design and Analysis (MS) 3.30 PM to 5.00 PM
Feb 01, 2017	Wednesday		Filters (MS) 9.30 AM to 11.00 AM			Introduction to RFIC Design (TKB) 1.45 PM to 3.15 PM		Basic Rx-Tx (TKB) 3.30 PM to 5.00 PM
Feb 02, 2017	Thursday		LNA (TKB) 9.30 AM to 11.00 AM			MIXER (TKB) 1.45 PM to 3.15 PM		VCO Design (TKB) 3.30 PM to 5.00 PM
Feb 03, 2017	Friday		Power Amplifier (TKB) 9.30 AM to 11.00AM			Frequency Synthesizer Part – I (TKB) 1.45 PM to 3.15 PM		Frequency Synthesizer Part –II (TKB) 3.30 PM to 5.00 PM
Feb 04, 2017	Saturday		RFIC Layout (TKB) 9.30 AM to 11.00 AM					

**Feedback & Valedictory**  
(3.45 PM to 5.00 PM)



(TKB: Prof. T K Bhattacharya, IC: Prof. Indrajit Chakrabarti, MS: Prof. Mrgank Sharad)

PRINCIPAL

Ingnan Institute of Technology and Science  
Deshmukhi, Dhongir Yadaori Dist-508 264

Note: Every Lecture will include 15 Minutes Discussion at the End



# VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

(Programmes Accredited by NBA and Certified by ISO 9001 : 2000)  
(Sponsored by Lavu Educational Society)



JAS-ANZ



Date: 05-11-2016  
From: vignan institute of technology and sciences  
Dr. C. Srinivasa Kumar,  
Principal, VITS

To,  
The Project Co-ordinator  
Project "T10KT", IIT Kharagpur  
Vikramshila Building,  
Ground floor, Kalidas Auditorium  
IIT Kharagpur, Kharagpur – 721302

Permission letter for Two Week ISTE STTP on CMOS, Mixed Signal and Radio Frequency VLSI Design.

This is to certify that Associate Prof. Mr. VijayKumar R. Urkude is a regular teaching employee in the department of ECE of our Institute with a designation of Associate Professor. He/ She is allowed to attend the workshop on CMOS, Mixed Signal and Radio Frequency VLSI Design under the National Mission on Education through ICT (MHRD, Govt. of India) from dated 30/01/2017 to 04/02/2017 in the vignan institute of technology and sciences (RC ID: 1126).

The Institute has no objection to his/ her participation in the workshop. We would make sure that no official duties will be delegated which may affect his/ her participation, during workshop.

Thank You,

Institute Head's Signature with Stamp:

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Vignan Institute of Technology and Science  
Deshmukhi, Bhongir Yadadri Dist-508 284

Remote center Coordinator's Signature



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Deshmukhi, Bhongir Yadadri Dist-508 284

## SPEAKERS PROFILE

Prof. T. K. Bhattacharya, Prof. Indrajit Chakrabarti, and  
Dr. Mrigank Sharad.



### Contact Addresses

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### Research Areas

- MEMS and Microsystems
- RF and Analog VLSI
- Nano- electronics
- Thinfilms
- Nano-scale Biosystems Engineering

### Awards & Honours

- IBM Faculty Award (2012)
- Design of Microfluidic system for controlled Drug delivery has been selected among top 50 by India innovation growth program 2016, by DST, Lockheed Martin , Indo-US S&T forum, Stanford Graduate Scho (2016)

### Member of Professional Bodies

- Member : IEEE



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#### Current Sponsored Projects

- **Project Title :** Development of cost effective non-invasive blood pressure measurement equipment for assessment of broader perspective of Cardiovascular  
**Principal Investigator :** T K Bhattacharyya  
**Co-Principal-Investigators :** G Saha , Dipankar Chakrabarti  
**Sponsor :** MHRD, Govt of India
- **Project Title :** Development of readout system for Muon system in CBM experiment at FAIR (NSC)  
**Principal Investigator :** Dr. T. K. Bhattacharyya  
**Co-Principal-Investigators :** Dr. Achintya Halder  
**Sponsor :** VECC, Kolkata/Govt. of India
- **Project Title :** Development of MEMS based components for RF applications  
**Principal Investigator :** Tarun Kanti Bhattacharyya  
**Co-Principal-Investigators :** B K Sarkar  
**Sponsor :** NPMASS
- **Project Title :** Development of MEMS based Accelerometers for Aerospace Application (NPMASS)  
**Principal Investigator :** T.K. Bhattacharyya  
**Co-Principal-Investigators :** S. Das  
**Sponsor :** NPMASS, ADA, Bangalore
- **Project Title :** MEMS based micropropulsion devices for micro satellite program  
**Principal Investigator :** T K Bhattacharyya  
**Co-Principal-Investigators :** S Das  
**Sponsor :** ISRO
- **Project Title :** Upgrading Facilities for MEMS design activities at National resource centre  
**Principal Investigator :** T.K. Bhattacharyya  
**Co-Principal-Investigators :** S. Das  
**Sponsor :** NPMASS, ADA, Bangalore
- **Project Title :** Design & Fabrication of high sensitivity micro machined Silicon tunneling accelerometer with micro-g resolution  
**Principal Investigator :** T.K. Bhattacharyya  
**Sponsor :** ISRO, IIT Kharagpur Cell 721302
- **Project Title :** Synthesis of functional groups for immobilization of functional proteins on MEMS based micro-sensor surfaces  
**Principal Investigator :** T.K. Bhattacharyya  
**Co-Principal-Investigators :** S. Das, T K Maiti  
**Sponsor :** Indo-Trento Program for Advanced Research
- **Project Title :** SPECIAL MANPOWER DEVELOPMENT PROGRAMME FOR CHIPS TO SYSTEM DESIGN (MDD)  
**Principal Investigator :** P K Biswas  
**Co-Principal-Investigators :** T K Bhattacharyya  
**Sponsor :** DeiTy (Govt. Of India)
- **Project Title :** Development and Realization of Silicon Tunneling Accelerometer  
**Principal Investigator :** Dr.T.K Bhattacharyya  
**Co-Principal-Investigators :** Dr. P.K Guha & Dr. S Das  
**Sponsor :** ISRO Inertial Systems Unit (IISU)
- **Project Title :** AVLSI Consortium  
**Principal Investigator :** Dr.T.K Bhattacharyya  
**Sponsor :** Multiple Industrial Organisation



*S. Subramanian*  
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<http://www.ecdept.iitkgp.ernet.in/index.php/home/faculty/indrajit>

#### Research Areas

- VLSI Design for Image and Video Processing and Communication

#### Member of Professional Bodies

- Member : IEEE

#### Current Sponsored Projects

- **Project Title :** Development of Interference Mitigation methods through Base Station Cooperation in Next Generation Wireless Broadband Mobile Communication Networks  
**Principal Investigator :** Dr. Suvra Sekhar Das  
**Co-Principal-Investigators :** Dr. I. Chakrabarti, Prof. D. Datta, Prof. S. Chakrabarti, Dr. G. Das  
**Sponsor :** MCIT, DIT, new Delhi
- **Project Title :** Scalable Video Coder using MCTF  
**Principal Investigator :** Dr. Indrajit Chakrabarti  
**Co-Principal-Investigators :** Prof. A.S. Dhar  
**Sponsor :** ISRO
- **Project Title :** Hardware Test Bed for Automated Breast Cancer Detection from Ultrasound Images  
**Principal Investigator :** Dr. Indrajit Chakrabarti  
**Co-Principal-Investigators :** Dr. Sumit Chakraborty (SSKM, Kolkata) and Dr. Rezaul Karim (CMSDH, Kolkata)  
**Sponsor :** MHRD



*Shubarna*  
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#### Patents

- System for Motion Estimation in Video Signals for Real Time Video Processing : Filed on 20/08/2014 at Patent Office, Kolkata: Application No.: 859/KOL/2014 (Ref : Ref: 2029/ASA/PP 1547/iikgp )
- System for Efficient VLSI Architectures for Adaptive Motion Estimation : filed on 30/04/2015 at Patent Office, Kolkata (Ref : Ref: 2235/ASA/PP-1703/IIT,Kgp, Application No.: 484/KOL/2015 )

#### Publications: 2015 - 2016

- An Efficient Constant Multiplier Architecture Based on Vertical-Horizontal Binary Common Sub-expression Algorithm for Reconfigurable FIR Filter Synthesis by I. Hatai, I. Chakrabarti and S. Banerjee *IEEE Transactions on Circuits and Systems-I: Regular Papers*, vol.62, pp.1071-1080 (2015)
- An Efficient VLSI Architecture of a Reconfigurable Pulse-Shaping FIR Interpolation Filter for Multistandard DUC by I. Hatai, I. Chakrabarti and S. Banerjee *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol.23, pp.1150-1154 (2015)
- Efficient architecture of adaptive rood pattern search technique for fast motion estimation by B.Biswas, R.Mukherjee and I.Chakrabarti *Microprocessors and Microsystems*, Vol. 39, pp. 200-209 (2015)
- High-speed low-power very -large-scale integration architecture for dual-standard deblocking filter by B.K.N. Srinivasa Rao, I. Chakrabarti and M. Nawaz Ahmad *IET Circuits, Devices and Systems*, vol. 9, pp. 377-383 (2015)
- Reconfigurable data parallel constant geometry fast Fourier transform architectures on Network-on-Chip by N. Prasad, S. Chattopadhyay and I. Chakrabarti *Microprocessors and Microsystems*, vol. 39, pp. 741-751 (2015)
- High Performance VLSI Architecture for Three Step Search Algorithm by R. Mukherjee, K. Sheth, A.S. Dhar, I. Chakrabarti and S. Sengupta *Circuits, Systems and Signal Processing*, vol.34, pp.1595-1612 (2015)
- Low-Delay Parallel Architecture for Fractal Image Compression by M. Panigrahy, I. Chakrabarti and A.S. Dhar *Circuits Systems and Signal Processing*, vol. 35, pp. 897-917 (2015)
- Modelling of Cooperative Spectrum Sensing over Rayleigh Fading without CSI in Cognitive Radio Networks by D. Bera, I. Chakrabarti and S.S. Pathak *Journal on Wireless Personal Communication (WPC)*, vol. 86, pp.1281-1297 (2016)
- An Efficient VLSI Architecture for the Enhanced Three Step Search Algorithm by B.Biswas, R.Mukherjee, P. Saha and I.Chakrabarti *Journal of The Institution of Engineers (India): series B*, (2015)

#### Publications: 2014 - 2015

- An Efficient VLSI Architecture of a Reconfigurable Pulse-Shaping FIR Interpolation Filter for Multistandard DUC by I. Hatai, I. Chakrabarti and S. Banerjee *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, 1-5 (2014)
- An Efficient VLSI Architecture for the Enhanced Three Step Search Algorithm by B.Biswas, R.Mukherjee, P. Saha and I.Chakrabarti *Springer Journal of The Institution of Engineers (India): series B (DOI: 10.10, (2015)*
- Efficient architecture of adaptive rood pattern search technique for fast motion estimation by B.Biswas, R.Mukherjee and I.Chakrabarti *Microprocessors and Microsystems (DOI: 10.1016/j.micpro.2015.02.004)*, (2015)
- High Performance VLSI Architecture for Three Step Search Algorithm by R. Mukherjee, K. Sheth, A.S. Dhar, I. Chakrabarti and S. Sengupta *Springer Circuits, Systems and Signal Processing*, (2014)



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#### Research Areas

- Nanoelectronics and VLSI Design (Digital/Mixed-Signal Circuit/Architecture)
- Application Specific Signal Processing
- Bioelectronics and Neuromorphic Computing
- Spintronics (Spin Memory and Logic)

#### Awards & Honours

- Student Innovator Award, Burton De Morgan Entrepreneurship Centre, Purdue University (2014)
- Best Poster Award, (Title: Boolean and Non Boolean Computing using Spin Devices) Index Review Center, USA (2012)
- Andrews Fellowship, Purdue University (2010)
- Prime Minister of India Gold Medal, IIT Kharagpur (2010)
- Institute Silver Medal (2010)
- Best Mtech Project (2010)
- Best Btech Project (2009)
- PhD work reported in MIT Technology Review (2012)
- PhD work contributed to Research \$ 5 Million Research Grants from DoD, SRC, USA (PI: Prof. K. Roy) (2015)

#### Current Sponsored Projects

- **Project Title :** Chip to System Design (C2SD)  
**Principal Investigator :** P. K. Biswas (Self : Co-Investigator)  
**Sponsor :** DEITY
- **Project Title :** Radiation Hardened Mixed Signal Circuits for Space Application  
**Principal Investigator :** Pradip Mandal



*Pradip Mandal*  
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**Co-Principal-Investigators : Mrigank Sharad**  
**Sponsor : ISRO**

**Patents**

- ELECTRONIC COMPARISON SYSTEMS : Granted (Ref : 20,150,347,896 )
- CLOCK BUFFERS WITH PULSE DRIVE CAPABILITY FOR POWER EFFICIENCY : Granted (Ref : 20,150,365,076 )

**Publications: 2014 - 2015**

- SPINDLE: SPINtronic Deep Learning Engine for Large-scale Neuromorphic Computing by G. S. Balasubramaniam, R. Venkaesan, M. Sharad, K. Roy A. Raghunathan *ACM Proceedings*, (2014)
- Dense, Energy-efficient All-Spin Cache Hierarchy using Shift-based Writes and Multi-Level Storage by R. Venkaesan, M. Sharad, K. Roy A. Raghunathan *ACM Proceedings*, (2015)
- Cache Design with Domain Wall Memories by R. Venkaesan, M. Sharad, C. Augustine, A. Roychoudhury, K. Roy, A. Raghunathan *ACM Proceedings*, (2014)
- Hierarchical Temporal Memory Based on Spin-Neurons and Resistive Memory for Energy-Efficient Brain-Inspired Computing by D. Fan, M. Sharad, A. Gengupta, K. Roy *IEEE Transaction on Neural Networks*, (2015)
- Energy Efficient Associative Computing Using Injection Locked Spin Torque Oscillators by M. Sharad, D. Fan, K. Roy *IEEE Transaction on Magnetics*, (2015)
- Injection Locked Spin Hall Oscillators for Non Boolean Computing by D. Fan, S. Maji, A. Sengupta, M. Sharad, K. Roy *IEEE Transaction on Magnetics (Submitted)*, (0)
- Spintronic Latch for Low Power On-Chip Clocking (Submitted) by M. Sharad, K. Roy *IEEE Electron Device Letter*, (2015)
- Design and Synthesis of Ultra Low Energy Spin- Memristor Threshold Logic by D. Fan, M. Sharad, K. Roy, *IEEE Trans. on Nanotechnology*, (2014)
- Spintronic Switches for Ultra Low Energy On-Chip and Inter-Chip Interconnects by M. Sharad, and K. Roy, *IEEE Electron Device Letters*, (2013)
- Exploring Spin Transfer Torque Devices for Unconventional Computing. by Roy, K., Fan, D., Kim, Y., Sharad, M., Paul, S., Mukhopadhyay, S. (2015). *Emerging and Selected Topics in Circuits and Systems, IEEE Journal on*, 5(1), 5-16. (2015)
- Exploring Ultra Low Power Global Clocking Scheme using Spin Torque Devices by M. Sharad, A. Sengupta, K. Roy *Electron Device Letter*, (2015)

**Publications: 2013 - 2014**

- Modelling and Simulation Based Study of Spintronic Switches for Ultra Low Energy Global Interconnects by M. Sharad, K. Roy *Journal of App. Phys.*, (2013)
- Energy Efficient Non-Boolean Computing Using Spin Neurons and Memristors by M. Sharad, D. Fan, K. Aitken, K. Roy *IEEE Transaction on Nanotechnology*, (2013)
- Dual-Pillar Spin-Torque Oscillator for Energy Efficient Computation by M. Sharad, K. Yogendra and K. Roy *Applied Phys. Lett*, (2013)
- Spin-neurons: A possible path to energy-efficient neuromorphic computers by M. Sharad, D. Fan, K. Roy *Journal of Applied Physics*, (2013)
- Ultra-low Energy Global Interconnects based on Spin Torque Switches by M. Sharad, X. Fong and K. Roy *IEDM (Conference)*, (2013)
- Energy-Efficient MRAM-Access Scheme Using Hybrid-Circuits Based on Spintronic-Sensors by M. Sharad, R. Venkaesan, X. Fong, A. Raghunathan and K. Roy *IEEE Sensors Conference*, (2013)

  
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## TWO-WEEK ISTE STTP ON CMOS, MIXED SIGNAL AND RADIO FREQUENCY VLSI DESIGN

### Participant Verification List for Certificate

Remote Center Name :- Vignan Institute of Technology and Science, Deshmukhi - Nalgonda

Workshop Coordinator's Name :- Vijaykumar R. Urkude

Remote Center Coordinator's Name :- Dr. N. Dinesh Kumar



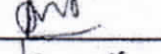
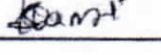
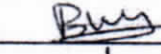

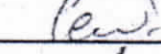

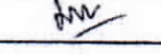





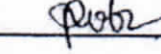
Participants are requested to verify their name, institute name .

Sl No	Participant ID	Participant Name	Sign
1	506	P.A.Harsha Vardhini	
2	568	RAJARAM JATOTHU	
3	871	anindhyakiran jaladi	
4	930	Jillella Venkateswara Rao	
5	1432	VEGURU PRAKASAM	
6	1475	PATRI UPENDER	
7	1568	Chintam V B Aditya Kumar	
8	1653	sandeep pingili	
9	1661	DUBBA SANDEEP	
10	1670	DHANIKA SRINIVAS	
11	1934	NAGESWARA RAO P	
12	2251	Vikas Tiwari	
13	2259	BENJARAM MADHUSUDHAN REDDY	
14	2263	K R ANUDEEP LAXMIKANTH	
15	2266	K MURALI CHANDRA BABU	
16	2273	KOTESWARAMMA NELAPATI	
17	2278	Vijayalaxmi Biradar	
18	2355	SATISH MARRU X	
19	2573	KALYANI RAMANABOINA	
20	3139	ILIAS MOHAMMAD	
21	4300	RAJESH AKULA	
22	4683	Anawruddin Mohammed	
23	4851	SATYANARAYANA MADDISANI	
24	4864	RAMAKRISHNA REDDY KAMURI	
25	4998	N Sudheer Kumar	
26	5139	PRASANNA SESHAPU	



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27	5161	prashanthi pinninti	
28	5189	shanthi priya	
29	5213	jhansi ande	
30	5274	s vamsi krishna	
31	5597	KRANTHI KUMAR ANDHE	
32	5598	SAGGAM DIVYASRI X	
33	5656	B Venkateshwar Rao	
34	5674	SSGN Srinivasa Rao	
35	5706	sateesh amarneni	
36	5719	PRAKASH BABU PENTAMALA	
37	5721	BALAGA BHAGAVATI RAO	
38	5748	kunapareddy koteswara rao	
39	5781	HARIKRISHNA PONNAM X	
40	5798	chakriya nayak daravathu	
41	5893	swapna chikkala	
42	6158	PADMAJA P	
43	6194	Padmavathi Kavuru	
44	6376	Pardeep Kumar	
45	6426	Pavankumar Nandyala	
46	6980	REHANA SIDDIQUI X	



  
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 Affiliated to JNTU Hyderabad. Programmes Accredited by NBA, Approved by AICTE, New Delhi  
 BANCET CODE : VQBT



**VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE: DESHMUKHI**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

**CMOS mixed signal and Radio frequency VLSI Design Workshop Attendance Sheet**

S.N	Name of the staff	College Name	30/01/2017	31/01/2017	01/02/2017	02/02/2017	03/02/2017	04/02/2017
1	Dr.Nali Dinesh Kumar	VITS	Hof	Hof	Hof	Hof	Hof	Hof
2	Dr.PalagiriAnjaniHarshaVardhini	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
3	Dr.VijayalaxmiBiradar	VITS	Narayana	Narayana	Narayana	Narayana	Narayana	Narayana
4	Dr.Jillella Venkateswara Rao	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
5	Mr.VijaykumarUrkude	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
6	Mrs.NelapatiKoteswaramma	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
7	Mrs.PydimarriPadmaja	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
8	Mr.Nageswara Rao P	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
9	Mr.VeguruPrakasam	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
10	Mr.PatriUpender	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
11	Mr.Sandeep Pingili	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
12	Mr.B.Madhusudhan Reddy	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya
13	Mr.Vikas Kumar Tiwari	VITS	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya	Vijaya



*Sankar*  
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EAMCET CODE : V01T

	DS	DS	DS	DS	DS	DS	DS
14 Mr. D. Srinivas	DS	DS	DS	DS	DS	DS	DS
15 D Sandeep	D Sandeep	D Sandeep	D Sandeep	D Sandeep	D Sandeep	D Sandeep	D Sandeep
16 D.BinduTushara	Bindu D	Bindu D	Bindu D	Bindu D	Bindu D	Bindu D	Bindu D
17 K.MuraliChandaraBabu	Mural	Mural	Mural	Mural	Mural	Mural	Mural
18 Mr. N. Pavan Kumar	PP	PP	PP	PP	PP	PP	PP
19 AppidiKavitha Reddy	Kavitha	Kavitha	Kavitha	Kavitha	Kavitha	Kavitha	Kavitha
20 Mr. P. Srinivasu	Praveen	Praveen	Praveen	Praveen	Praveen	Praveen	Praveen
21 Mr. Vuyyala Shankar	Vuyyala	Vuyyala	Vuyyala	Vuyyala	Vuyyala	Vuyyala	Vuyyala
22 Mrs.R.B.Kalyani	Kalyani	Kalyani	Kalyani	Kalyani	Kalyani	Kalyani	Kalyani
23 Mr. B. Ravi Kumar	R	R	R	R	R	R	R
24 Mr. Ch. Vamshi Krishna	Vamshi	Vamshi	Vamshi	Vamshi	Vamshi	Vamshi	Vamshi
25 Mr. S. Praveen	Praveen	Praveen	Praveen	Praveen	Praveen	Praveen	Praveen
26 Ms. M. Kavitha Rani	Kavitha	Kavitha	Kavitha	Kavitha	Kavitha	Kavitha	Kavitha
27 Ms. MaddiShireesha	Maddi	Maddi	Maddi	Maddi	Maddi	Maddi	Maddi
28 Ms. Y. Jyothi	Y Jyothi	Y Jyothi	Y Jyothi	Y Jyothi	Y Jyothi	Y Jyothi	Y Jyothi
29 Ms.D.Swarupa Rani	D Swarupa	D Swarupa	D Swarupa	D Swarupa	D Swarupa	D Swarupa	D Swarupa
30 G.Upendar	G Upendar	G Upendar	G Upendar	G Upendar	G Upendar	G Upendar	G Upendar



*Shukama*  
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# VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE



(An ISO 9001 : 2000 Certified Institution)

(Sponsored by Lavu Educational Society)

JAS-ANZ



Date: 05-11-2016

From,  
Dr. C. Srinivasa Kumar,  
Principal, VITS

To,  
The Project Co-ordinator  
Project "T10KT", IIT Kharagpur  
Vikramshila Building,  
Ground floor, Kalidas Auditorium  
IIT Kharagpur, Kharagpur – 721302

## Permission letter for Two Week ISTE STTP on CMOS, Mixed Signal and Radio Frequency VLSI Design.

This is to certify that the following participants are regular teaching employees of our Institute with one of the following designation :- (Jr. Lecturer/ Lecturer/Sr. Lecturer/Asst. Professor/Associate Professor/ Professor/HOD/ Reader/Vice- Principal/Visiting Faculty/ Teaching Fellow).

SL No.	Name	Designation	Department
1	Mr. D. Sandeep	Assistant Professor	Electronics & Communication Engg.
2	Mr. K. Murali Chandra Babu	Assistant Professor	Electronics & Communication Engg.
3	Mr. Vikas Kumar Tiwari	Assistant Professor	Electronics & Communication Engg.
4	Mr. K.R. Anudeep Laxmikanth	Assistant Professor	Electronics & Communication Engg.
5	Mr. B. Madhusudhan Reddy	Assistant Professor	Electronics & Communication Engg.
6	Mr. V. Shankar	Assistant Professor	Electronics & Communication Engg.
7	Mrs. R. Swapna	Assistant Professor	Electronics & Communication Engg.
8	Mr. D. Srinivas	Assistant Professor	Electronics & Communication Engg.
9	Mr. A. Sriram	Assistant Professor	Electronics & Communication Engg.
10	Ms. Rehana Siddiqui	Assistant Professor	Electronics & Communication Engg.

The faculty members are allowed to attend the workshop on CMOS, Mixed Signal and Radio Frequency VLSI Design under the National Mission on Education through ICT (MHRD, Govt. of India) from dated 30/01/2017 to 04/02/2017 in the Vignan Institute of Technology & Science, Deshmukhi, RC ID 1126.

The Institute has no objection to his/ her participation in the workshop. We would make sure that no official duties will be delegated which may affect his/ her participation, during workshop.

Thank You,

Institute Head's Signature with Stamp  
**PRINCIPAL**

Vignan Institute of Technology and Science  
Deshmukhi, Nalgonda-508 284.



*[Handwritten Signature]*  
**PRINCIPAL**

Vignan Hills, Deshmukhi Village, Pochampally Mandal, Nalgonda District - 508 284, A.P.  
Ph : 08685 - 205101, 226128 / 133 / 134, Cell : 98663 99776 / 861, Fax : 08685 - 226149  
www.vignanits.ac.in, email : info@vignanits.ac.in



# VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

(An ISO 9001 : 2000 Certified Institution)  
(Sponsored by Lavu Educational Society)



IAS-ANZ



Date: 05-11-2016

From,  
Dr. C. Srinivasa Kumar,  
Principal, VITS

To,  
The Project Co-ordinator  
Project "T10KT", IIT Kharagpur  
Vikramshila Building,  
Ground floor, Kalidas Auditorium  
IIT Kharagpur, Kharagpur – 721302

Permission letter for Two Week ISTE STTP on CMOS, Mixed Signal and Radio Frequency VLSI Design.

This is to certify that the following participants are regular teaching employees of our Institute with one of the following designation :- (Jr. Lecturer/ Lecturer/Sr. Lecturer/Asst. Professor/Associate Professor/ Professor/HOD/ Reader/Vice- Principal/Visiting Faculty/ Teaching Fellow).

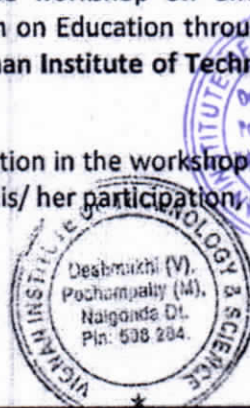
SL No.	Name	Designation	Department
1	Mr. M. Satish	Assistant Professor	Electronics & Communication Engg.
2	Mr. N.Pavan Kumar	Assistant Professor	Electronics & Communication Engg.
3	Ms. R.B. Kalyani	Assistant Professor	Electronics & Communication Engg.
4	Mr. B.V. Chowdary	Associate Professor	Computer Science Engg.
5	Mr. N. Sreekanth	Associate Professor	Computer Science Engg.
6	Mr. G. Yedukondalu	Associate Professor	Computer Science Engg.
7	Mr. UNP Gangadhar Raju	Associate Professor	Computer Science Engg.
8	Mr. N. Sri Anjaneya	Associate Professor	Computer Science Engg.
9	Mr. V. Yadaiah	Associate Professor	Computer Science Engg.
10	Mr. B.H. Ravi Krishna	Associate Professor	Computer Science Engg.

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Thank You,

Institute Head's Signature with Stamp  
**PRINCIPAL**



*[Signature]*  
**PRINCIPAL**

Vignan Institute of Technology and Science  
Deshmukhi, Bhongir, Yadadri Dist-508 204





# VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE



(An ISO 9001 : 2000 Certified Institution)  
(Sponsored by Lavu Educational Society)

JAS-ANZ



Date: 05-11-2016

From,  
**Dr. C. Srinivasa Kumar,**  
Principal, VITS

To,  
The Project Co-ordinator  
Project "T10KT", IIT Kharagpur  
Vikramshila Building,  
Ground floor, Kalidas Auditorium  
IIT Kharagpur, Kharagpur – 721302

Permission letter for Two Week ISTE STTP on CMOS, Mixed Signal and Radio Frequency VLSI Design.

This is to certify that the following participants are regular teaching employees of our Institute with one of the following designation :- (Jr. Lecturer/ Lecturer/Sr. Lecturer/Asst. Professor/Associate Professor/ Professor/HOD/ Reader/Vice- Principal/Visiting Faculty/ Teaching Fellow).

SL No.	Name	Designation	Department
1	Dr. J.V.Rao	Professor	Electronics & Communication Engg.
2	Dr. P.A.Harshavardhini	Professor	Electronics & Communication Engg.
3	Ms. B. Vijayalaxmi	Associate Professor	Electronics & Communication Engg.
4	Mrs. P. Padmaja	Assistant Professor	Electronics & Communication Engg.
5	Mr. V. Prakasam	Assistant Professor	Electronics & Communication Engg.
6	Mr. P.Sandeep	Assistant Professor	Electronics & Communication Engg.
7	Mr. P. Nageswara Rao	Assistant Professor	Electronics & Communication Engg.
8	Mr. P. Upender	Assistant Professor	Electronics & Communication Engg.
9	Mrs. N. Koteswaramma	Assistant Professor	Electronics & Communication Engg.
10	Mrs. D. Bindu Tushara	Assistant Professor	Electronics & Communication Engg.

The faculty members are allowed to attend the workshop on CMOS, Mixed Signal and Radio Frequency VLSI Design under the National Mission on Education through ICT (MHRD, Govt. of India) from dated 30/01/2017 to 04/02/2017 in the Vignan Institute of Technology & Science, Deshmukhi, RC ID 1126.

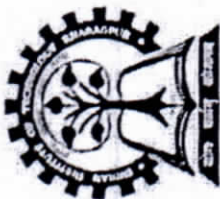
The Institute has no objection to his/ her participation in the workshop. We would make sure that no official duties will be delegated which may affect his/ her participation, during workshop.

Thank You,  
*[Signature]*  
Institute Head's Signature with Stamp  
**PRINCIPAL**

Vignan Institute of Technology and Science  
Deshmukhi, Nalgonda-508 284.



*[Signature]*  
**PRINCIPAL**  
Vignan Institute of Technology and Science  
Deshmukhi, Bhongir Yadadri Dist-508 284.



# Certificate

This is to certify that  
**Dr. B. Vijayalaxmi**  
of

**Vignan Institute of Technology and Science, Deshmukhi - Nalgonda**

participated in a Two-week ISTE STTP on  
**CMOS, Mixed Signal and Radio frequency VLSI Design**  
Conducted by Indian Institute of Technology Kharagpur  
from 30<sup>th</sup> January, 2017 to 4<sup>th</sup> February, 2017

*(Online activity equivalent to one week from 26<sup>th</sup> December, 2016 to 4<sup>th</sup> February, 2017 and  
physical participation at Remote Center from 30<sup>th</sup> January to 4<sup>th</sup> February, 2017)*

Grade  Very Good

This workshop was held under the National Mission on Education through ICT (MHRD)

**A. Goswami**  
**Prof. A. Goswami**  
Dean, Continuing Education  
IIT Kharagpur

**T.K. Bhattacharyya**  
**Prof. T.K. Bhattacharyya**  
Course Coordinator  
IIT Kharagpur

**Vijaykumar R. Urkude**  
Workshop Coordinator  
**Vignan Institute of Technology and Science, Deshmukhi**

*B. Subramas*



PRINCIPAL

Vignan Institute of Technology and Science  
Deshmukhi, Bhongir, Yadadri Dist-508 264



**VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE**  
 Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Tlangana - 508 284.  
 Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi  
**EAMCET CODE : VGNT**



## WORKSHOP REPORT

This report provides a brief summary of the workshop organized by department of Electronics and Communication Engineering, IIT Kharagpur and IIT Bombay. This organized a Two week ISTE workshop on “CMOS mixed signal and Radio frequency VLSI Design” on 30<sup>th</sup> Jan- 4<sup>th</sup> Feb, 2017, from 10.00 am to 5.00 pm in the Seminar Hall, 1<sup>st</sup> Floor, VITS Under the National Mission on Education through ICT (MHRD, Govt. of India). The workshop brought together over 30 Faculty of ECE. The VITS Principal graced the occasion and wished grand success for the program, HOD-ECE.

Design, analysis and implementation of CMOS Mixed Signal and RFIC are attractive topics both in academic and Industry. CMOS RFIC design is a challenging and critical engineering task particularly in the arena of deep sub-micron technology. There is a strong demand of manpower having thorough knowledge and hands on experiences in VLSI design using state of the art EDA tools. The course focused on the above issues along with case studies..

**Resource Person: Prof. T. K. Bhattacharya, Prof. Indrajit Chakrabarti, and Dr. Mrigank Sharad.**

**Duration: 5 hrs per day**

Date	Days	Morning		Afternoon	
Jan 30, 2017	Monday	Inauguration 9.00 AM to 9.30 AM	Introductory Remark (TKB, IC, MS) 9.30 AM to 11.00 AM	Basic Analog Design Part-I (MS) 11.15 AM to 12.45 PM	Basic Analog Design Part-II (MS) 1.45 PM to 3.15 PM
Jan 31, 2017	Tuesday	Basic Digital VLSI Design Part-I (IC) 9.30 AM to 11.00 AM		Basic Digital VLSI Design Part-II (IC) 11.15 AM to 12.45 PM	Low Power Signal Processing Circuits (IC) 1.45 PM to 3.15 PM
Feb 01, 2017	Wednesday	Filters (MS) 9.30 AM to 11.00 AM		ADG (MS) 11.15 AM to 12.45 PM	Introduction to RFIC Design (TKB) 1.45 PM to 3.15 PM
Feb 02, 2017	Thursday	LNA (TKB) 9.30 AM to 11.00 AM		Motion Estimation Architecture (IC) 11.15 AM to 12.45 PM	MIXER (TKB) 1.45 PM to 3.15 PM
Feb 03, 2017	Friday	Power Amplifier (TKB) 9.30 AM to 11.00 AM		QUIZ 11.15 AM to 12.45 PM	Frequency Synthesizer Part - I (TKB) 1.45 PM to 3.15 PM
Feb 04, 2017	Saturday	RFIC Layout (TKB) 9.30 AM to 11.00 AM		RF System Level Simulation and Analysis (TKB) 11.15 AM to 12.45 PM	Frequency Synthesizer Part - II (TKB) 1.45 PM to 3.15 PM
		Tea break 11.00 AM to 11.15 AM		Lunch Break 12.45 PM to 1.45 PM	
				Tea Break 3.15 PM to 3.30 PM	
				<b>Feedback &amp; Valedictory</b> (1.45 PM to 3.00 PM)	



*Shankar*  
**PRINCIPAL**

Vignan Institute of Technology and Science  
 Deshmukhi, Bhongir Yadadri Dist-508 284



# VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

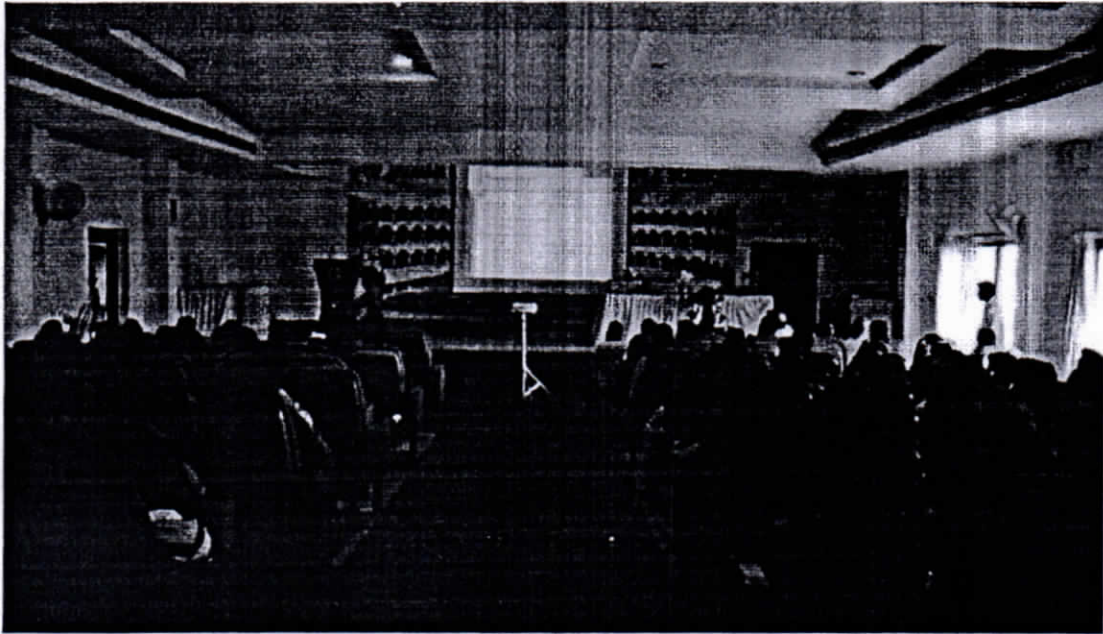
Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi

EAMCET CODE : VGNT



**Topics covered:** Topics such as design & implementation methodology of analog front-end, data converters, digital VLSI, RF circuits & transceiver were covered during this STTP.

## Photo gallery:



Vijay



*Saturna*

PRINCIPAL

Vignan Institute of Technology and Science  
Deshmukhi, Bhongir Yadadri Dist-508 284



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

(An ISO 9001 : 2000 Certified Institution)

(Sponsored by Lavu Educational Society)



JAS-ANZ



Deshmukhi

10-05-2017

To

The Principal,

Vignan Institute of Technology and Science,

Deshmukhi.

Sir,

**Subject:** Permission for conducting workshop on **LAB VIEW used in MULTI DISCIPLINARY** -  
Reg.

We, the Department of Electrical & Instrumentation Engineering would like to bring to your kind notice that based on the requirements of fulfillment of the faculty development, we are planning to conduct a Workshop on the topic "**LAB VIEW used in MULTI DISCIPLINARY** " in association with National Instruments ,Bangalore for faculty from 25<sup>th</sup> - 30<sup>th</sup> June, 2017. In this regard we request you to grant permission to conduct the program.

Thanking you

  
Coordinator

  
Yours faithfully,

Head of the Department



  
**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (Rd),  
Yadadri Bhuvanagiri Dist-508 201



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

(An ISO 9001 : 2000 Certified Institution)  
(Sponsored by Lavu Educational Society)



Date: 10-05-2017

To

Mr. Vasanth,  
Application Engineer,  
Bangalore.

Sub: Invitation as a Resource person to conduct workshop in our esteemed Organization- Reg

Respected Sir,

We are one of the 8 well reputed engineering institutions under Vignan group of institutions, situated in Deshmukhi about 6km from Ramoji film city. We have B.Tech and M.Tech courses in many branches of engineering, including Electronics & Instrumentation engineering. We have around 500 students including Bachelors and Masters. We would like our faculty to get exposed to industrial environment and more importantly industry practices, which would help them, upon their entry into any industry, to get set in the industry environment faster and deliver their jobs efficiently.

I am Pleased to invite you as a Resource person to conduct workshop in our esteemed organization for the academic year 2017-2018 in your specialized area. This would be a great platform for our faculty to learn from your experiences, and can enhance their knowledge.

With Best Regards,

  
HoD, EIE



PRINCIPAL  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Posthumatra, (Muthi,  
Yadadi, Bhuvangur Taluk, Bellary





VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

(An ISO 9001 : 2000 Certified Institution)  
(Sponsored by Lavu Educational Society)



JAS-ANZ



Date: 10-05-2017

To

Mr. Suneel,  
Senior Engineer,  
Bangalore.

Sub: Invitation as a Resource person to conduct workshop in our esteemed Organization- Reg

Respected Sir,

We are one of the 8 well reputed engineering institutions under Vignan group of institutions, situated in Deshmukhi about 6km from Ramoji film city. We have B.Tech and M.Tech courses in many branches of engineering, including Electronics & Instrumentation engineering. We have around 500 students including Bachelors and Masters. We would like our faculty to get exposed to industrial environment and more importantly industry practices, which would help them, upon their entry into any industry, to get set in the industry environment faster and deliver their jobs efficiently.

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With Best Regards,

  
HoD, EIE



  
PRINCIPAL  
Vignan Institute of Technology and Science  
(V) Deshmukhi, Pochempanny (Mdl),  
Yadadri Bruvanagiri Dist., 508 284.

---

**INVITATION FOR "LAB VIEW used in MULTI DESCIPLAINARY" PROGRAM**

---

Eie Vits <vits.eie@gmail.com>  
To: vasanth425@gmail.com

Wed, May 10 2017 at 11:50 AM

Sir,

It gives me immense pleasure to invite you to our Educational Institution as a Resource Person for a certificate course for Faculty which is scheduled from 25<sup>th</sup> - 30<sup>th</sup> June, 2017 on the topic '**LAB VIEW used in MULTI DESCIPLAINARY**'. The program begins from time 9:00 am to 5:00pm Your immense and practical experience in the domain is very much useful for the Faculty to develop their skills and they will be fortunate enough to have an illustrious person like you to update and guide them in a right path of their career. Hence, I am looking forward for your presence on our campus and ignite our minds.

Thanking you

Yours faithfully,  
Mrs.S.Prasanna  
Coordinator



A handwritten signature in blue ink, appearing to read "S. Prasad".

**PRINCIPAL**  
Vignan Institute of Technology and Science  
(Vi) Deshmukhi, Pochampally (H),  
Yadadri Bhuvanagiri Dist.-508 284.





Eie Vits <vits.eie@gmail.com>

---

**ACCEPTANCE OF INVITATION FOR “LAB VIEW used in MULTI DISCIPLINARY” PROGRAM**

---

vasanth<vasanth425@gmail.com>  
To: vits.eie@gmail.com

Mon, May 15, 2017 at 10:30 AM

I am glad to a part with an elite educational institution like yours and deliver speech on the “**LAB VIEW used in MULTI DISCIPLINARY**”. I delightfully accept your invitation and will make my presence there with the expertise and exposure of the subject.

**With regards,**

Vasanth,  
Application Engineer,  
Bangalore.



**PRINCIPAL**  
Vignan Institute of Technology and Science  
(V) Deshmukhi, Pachampally (A),  
Tadatri Bhuvanagiri, Dist-508 201,  
INDIA



---

**INVITATION FOR "LAB VIEW used in MULTI DISCIPLINARY" PROGRAM**

---

**Eie Vits** <vits.eie@gmail.com>  
To: suneel.kumar@gmail.com

Wed, May 10 2017 at 10:20 AM

Sir,

It gives me immense pleasure to invite you to our Educational Institution as a Resource Person for a certificate course for Faculty which is scheduled from 25<sup>th</sup> - 30<sup>th</sup> June, 2017 on the topic '**LAB VIEW used in MULTI DISCIPLINARY**'. The program begins from time 9:00 am to 5:00pm Your immense and practical experience in the domain is very much useful for the Faculty to develop their skills and they will be fortunate enough to have an illustrious person like you to update and guide them in a right path of their career. Hence, I am looking forward for your presence on our campus and ignite our minds.

Thanking you

Yours faithfully,  
Mrs.S.Prasanna  
Coordinator



**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VI) Deshmukhi, Pochampally (HdQ),  
Yadadri Bhuvanagiri Dist.-508 284.



---

**ACCEPTANCE OF INVITATION FOR “*LAB VIEW used in MULTI DISCIPLINARY*” PROGRAM**

---

suneel<suneel.kumar@gmail.com>  
To: vits.eie@gmail.com

Mon, May 15, 2017 at 5:35 PM

I am glad to a part with an elite educational institution like yours and deliver speech on the “*LAB VIEW used in MULTI DISCIPLINARY*”. I delightfully accept your invitation and will make my presence there with the expertise and exposure of the subject.

**With regards,**

Mr. Suneel,  
Senior Engineer,  
Bangalore.



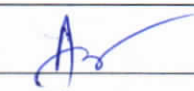











**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VI) Dashmukhi, Ponnampally (Mdt),  
Yadadri Bhuvanagiri Dist -506 284.

**VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE**  
**DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING**

20<sup>th</sup> June 2017

**CIRCULAR**

All the Teaching staff members are here by informed that 25<sup>th</sup>- 30<sup>th</sup> June, 2017, our Department is conducting workshop on "LAB VIEW used in MULTI DESCIPLAINARY " by Mr. Vasanth, Application Engineer and Mr. Suneel, Senior Engineer National Instrumentation ,Bangalore.

S.No.	Name of the full time teacher	Signature
1	CH V B Aditya Kumar	
2	Dr.Pachiyannan M	
3	K.Padmavathi	
4	K.Prem Sagar	
5	S.Prasanna	
6	P.Prashanthi Reddy	
7	B.Rajesh	
8	A.Jhansi Rani	
9	S.Vamshi Krishna	
10	K.Prasad Babu	
11	PVP Kishore	
12	P.Sai Kalyan	

  
**HOD-EIE**

cc: Principal for kind information,  
: All EIE Staff.  
: Transport Department

  
**PRINCIPAL**  
Vignan Institute of Technology and Science  
(Vi) Debnur, Tumkur, Karnataka (India),  
Yadadri Road, Tumkur - 575 004.



JAS-ANZ



## Program Schedule

DAY1: Introduction to Lab VIEW

Examples with Lab VIEW

Hands on Basics

Day 2: Introduction to Graphical system Design

Platform for Engineering Education using Lab VIEW

Hands on Graphical system design

Day 3: Hands on Training on Lab VIEW Fundamentals

Hands on Training on Lab VIEW Fundamentals

Day 4: Hands on Training on NI Multisim for circuits Education

Hands on Training on NI DAQ & Multisim Integration

Day 5: Hands on Training on NI Vision module Software

Hands on Training on RIO Architecture for Embedded Applications



*Sanku*

**PRINCIPAL**  
Vignan Institute of Technology and Science  
(Vi) Deshmukhi, Pochampally (Mcdl),  
Yadadri Bhuvanagiri Dist.-508 284.

### **About Institute:**

Vignan Institute of Technology & Science, established in 1999, is a fast growing technical institution in Andhra Pradesh under the auspices of the well known Vignan Group of Institutions. VITS is offering five under Educational graduate programs and six post graduate programs includes MBA & MCA. Currently VITS is handling research projects worth of Rs.0.6 crore. Our student of VITS won gold medal offered by JNTUH for academic excellence. VITS has got in association with number of organizations including JIT-Hyderabad. The Vignan Group is synonymous with quality engineering education having its institutions across the length and breadth of Andhra Pradesh. VITS is situated near the world renowned Ramoji film city, in an idyllic spot amidst a vast area of 350 acres with sylvan surroundings.

### **Mission & Vision**

To provide high quality academic programs, training activities, research facilities supported by continuous industry, institute interaction aims at promoting employability, leadership, well being and technological development of the Region, State and Nation.

### **About the Department**

The Department of EIE was started in the year 2008 with a VISION "To turn out state of the art technologists in the area of Instrumentation engineering capable of meeting the challenging the needs of industry in instrumentation and to create a 'centre of excellence' in Virtual Instrumentation".

### **Chief Patron**

Dr. L. Rathaiah, Chairman Vignan Group of Institutions

### **Patron**

Mr. B. Shraavan, CEO  
Vignan Group of Institutions, Hyderabad

### **Steering Committee**

Dr. M Venkata Ramana, Principal, VITS  
Prof. M. Premila, Principal, VMTW  
Prof. G.V.Rao, Director, ASL (Retd.)  
Prof. B. Shankar Rao, Director, ASL (Retd.)

### **Organizing Secretary**

Mr. Ch. V.B. Aditya Kumar, HOD  
Electronics & Instrumentation Engineering  
Mobile: (0)9849439683

### **Co-ordinator**

Mrs. S. Prasanna, Assoc. Prof  
Department of Electronics and  
Instrumentation Engineering  
Mobile: (0)8008173463

### **Organizing Committee Members**

Mr. K. Premeasagar  
Mr. S. Vamsi Krishna  
Ms. P. Prashanthi Reddy  
Mr. B. Rajesh  
A. Jhansi Rani  
Mrs. K. Padmavathi

### **Resource Persons:**

Mr. Vasanth,  
Application Engineer,  
Bangalore

### **About the LABVIEW :**

LabVIEW offers a graphical programming approach that helps you visualize every aspect of your application, including hardware configuration, measurement data, and debugging. This visualization makes it simple to integrate measurement hardware from any vendor, represent complex logic on the diagram, develop data analysis algorithms, and design custom engineering user interfaces.

### **Course Contents**

- Design Smart Machines or Industrial Equipment
- Teach Engineering Students
- Measure Physical Systems with Sensors or Actuators
- Validate or Verify Electronic Designs
- Develop Production Test Systems

### **Project using LABVIEW**

All the participants will be given a sample project as part of course, which will help them to apply their knowledge and skills gained in the course. This will be useful to handle their projects.



**PRINCIPAL**  
Vignan Institute of Technology & Science  
(No) 576001  
Dist. West Godavari

**REGISTRATION FORM**

Name..... *K. Prasad Babu*  
Designation..... *Asst. Prof.*  
Department..... *B.C.E*  
College Name..... *S.V.T.S.M.O.J*  
Address for Communication

..... *S. T. S. V. S. M. O. J. Hyderabad*  
.....  
.....

Mobile Number..... *9912992961*  
Email..... *prasadbabu@svtsmoj.ac.in*  
**Payment Details:**  
Cash: Rs..... *3000/-*  
Date..... *25.06.17*

**Any other information:**  
.....  
.....  
.....

Place: *Hyderabad*  
Date: *26/06/17*  
Applicant's Signature *[Signature]*

**Registration : Important Dates**  
> Last date for receiving Applications 14 June 2017.  
> The registration fee is Rs. 300/- per participant and it can be paid by cash. For any clarification the coordinators may be contacted.

**Who can participate:**  
The workshop will be beneficial to the Faculty of all branches to develop industrial applications using PLC and SCADA Software.

**Venue:**  
Vignan Institute of Technology & Science

**Contact Persons:**  
Mrs. S. Prasanna  
Assoc. Professor,

  
*[Handwritten Signature]*  
**PRINCIPAL**  
Vignan Institute of Technology and Science  
(VIT) Deshpande Institute of Technology and Science  
Vignan Institute of Technology & Science  
Vignan Institute of Technology & Science

**A One Week workshop**

**On**

**LAB VIEW used in MULTI  
DISCIPLINARY**  
25<sup>th</sup> - 30<sup>th</sup> June, 2017

Chief Coordinator  
**Mr. Ch. V.B. Aditya Kumar**  
HOD, Dept. of EIE, VITS

Coordinator  
**Mrs.S.Prasanna**  
Assoc. Professor, Dept. of EIE, VITS

Organized by



Department of  
**Electronics and Instrumentation Engineering**

**VIGNAN INSTITUTE OF  
TECHNOLOGY AND SCIENCE**  
Near Ramoji film City, Deshmukhi (V),  
Pochampally(M), Nalgonda District - 508284.  
(Affiliated to JNTUH, Accredited by NBA, AICTE)

**REGISTRATION FORM**

Name..... K. Pankaj Babu  
Designation..... Asst. Prof  
Department..... EIT  
College Name..... CNRCE  
Address for Communication  
..... CNR, Hyderabad  
.....  
.....  
Mobile Number..... 7075181104  
Email..... benyasaou@gmail.com

**Payment Details:**

Cash: Rs..... 300/-  
Date.....

**Any other information:**

.....  
.....  
.....

Place: Hyd

Date: \_\_\_\_\_ Applicant's Signature

**Registration : Important Dates**

- > Last date for receiving Applications 14 June 2017.
- > The registration fee is Rs. 300/-per participant and it can be paid by cash. For any clarification the coordinators may be contacted.

**Who can participate:**

The workshop will be beneficial to the Faculty of all branches to develop industrial applications using PLC and SCADA Software.

**Venue:**

Vignan Institute of Technology & Science

**Contact Persons:**

**Mrs. S. Prasanna**  
Assoc. Professor,



*S. Prasanna*

PRINCIPAL  
Vignan Institute of Technology and Science

*A One Week workshop*

*On*

**LAB VIEW used in MULTI  
DISCIPLINARY**

25<sup>th</sup> - 30<sup>th</sup> June, 2017

Chief Coordinator

**Mr. Ch. V.B. Aditya Kumar**  
HOD, Dept. of EIE, VITS

Coordinator

**Mrs.S.Prasanna**  
Assoc. Professor, Dept. of EIE, VITS

Organized by



Department of  
**Electronics and Instrumentation Engineering**

**VIGNAN INSTITUTE OF  
TECHNOLOGY AND SCIENCE**  
Near Ramoji film City, Deshmukhi (V),  
Pochampally(M), Nalgonda District - 508284.  
(Affiliated to JNTUH, Accredited by NBA, AICTE)



**VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE  
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING**

**LAB VIEW used in MULTI DESCiplinary Workshop Attendance Sheet from 25<sup>th</sup> - 30<sup>th</sup> June, 2017**

VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE: DESHMUKHI											
DEPARTMENT OF ELECTRONICS INSTRUMENTATION ENGINEERING											
LAB VIEW used in MULTI DESCiplinary Workshop Attendance Sheet Day Wise											
S.N	Name of the staff	College Name	25/06/2017	26/06/2017	27/06/2017	28/06/2017	29/06/2017	30/06/2017			
1	CH V B Aditya Kumar	VITS	Att	Att	Att	Att	Att	Att			
2	Dr. N.V.R Vikarm G	VITS	mb	mb	mb	mb	mb	mb			
3	K.Bhavani	VITS	K. Bhavani	K. Bhavani	K. Bhavani	K. Bhavani	K. Bhavani	K. Bhavani			
4	K.Padmavathi	VITS	VP	VP	VP	VP	VP	VP			
5	K.Prem Sagar	VITS	VP	VP	VP	VP	VP	VP			
6	S.Prasanna	VITS	S	S	S	S	S	S			
7	P.Prashanthi Reddy	VITS	VP	VP	VP	VP	VP	VP			
8	B.Rajesh	VITS	VP	VP	VP	VP	VP	VP			
9	CH Shanthi Priya	VITS	VP	VP	VP	VP	VP	VP			
10	A.Jhansi Rani	VITS	VP	VP	VP	VP	VP	VP			
11	S.Vamshi Krishna	VITS	VP	VP	VP	VP	VP	VP			
12	P.Anitha	VITS	VP	VP	VP	VP	VP	VP			
13	K. Prasad Babu	Greeting University	VP	VP	VP	VP	VP	VP			
14	K. padmalakshmi	CVR	VP	VP	VP	VP	VP	VP			
15	D. Sankar	Critem	VP	VP	VP	VP	VP	VP			

Sankar

PRINCIPAL  
Vignan Institute of Technology and Science  
Department of Technology (M.D.)  
Palada (Vijayanagara Dist - 508 284)



# VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Department of Electronics and Instrumentation Engineering

## WORKSHOP/FDP FEEDBACK

Name of the Faculty: K. Prasad Babu Date: 30-06-17

Name of the Workshop/FDP: Lab visits and in multidisciplinary

College Name: Gitam's

Please agree/disagree with the following statements:

	Excellent	Good	Average	Poor	Remarks
The workshop facilities were comfortable and appropriate.	/				
Contents of the modules		/			
Aspects of program to bridge the gap between theory and application	/				
The length of the workshop was right.	/				
The workshop materials provided were appropriate and helpful		/			
Time management		/			
The instructor was knowledgeable in the subject area	/				
The hands-on exercises supported the lecture and discussion		/			
Overall quality of program	/				



*K. Prasad Babu*  
**PRINCIPAL**  
Vignan Institute of Technology and Science  
(V) Deshmukhi, Peddampally (Rd),  
Yadadri (E) Hyderabad - 508 234.

*K. Prasad Babu*  
Signature of the Faculty

# VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Department of Electronics and Instrumentation Engineering

## WORKSHOP/FDP FEEDBACK

Name of the Faculty: D. Satish

Date: 30-06-17

Name of the Workshop/FDP: Lab oriented in Multidisciplinary.

College Name: VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Please agree/disagree with the following statements:

	Excellent	Good	Average	Poor	Remarks
The workshop facilities were comfortable and appropriate.	✓				
Contents of the modules	✓				
Aspects of program to bridge the gap between theory and application		✓			
The length of the workshop was right.		✓			
The workshop materials provided were appropriate and helpful	✓				
Time management			✓		
The instructor was knowledgeable in the subject area	✓				
The hands-on exercises supported the lecture and discussion		✓			
Overall quality of program		✓			



*Satish*  
PRINCIPAL  
Vignan Institute of Technology and Science  
(V), Deshmukhi, Pochampally (M),  
Yadadri Bhuvanagiri Dist. 508 284.

*Satish*  
Signature of the Faculty



# VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Vignan Hills, Deshmukhi(V), Pochampally(M), near Ramoji Film City Nalgonda(dist)-508284

## Certificate of Participation

This is to Certify that Mr /Ms B.Rajesh Designation Asst. Professor

From VITS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

from 25<sup>th</sup> - 30<sup>th</sup> June, 2017.

Coordinator

HOD



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(VI) Deshmukhi, Pochampally (M),  
Yadadri Bhuvanagiri Dist-508 284.



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## Certificate of Participation

This is to Certify that Mr /Ms S. Prasanna Designation Asst. Professor

From VTIS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

from 25<sup>th</sup> - 30<sup>th</sup> June, 2017.

Coordinator



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HOD

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## Certificate of Participation

This is to Certify that Mr /Ms K.Padmavathi Designation Asst. Professor

From VITS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

from 25<sup>th</sup> - 30<sup>th</sup> June, 2017.



*S. Subramanyam*

PRINCIPAL  
Vignan Institute of Technology and Science  
(M) Pochampally (M)  
(V) Deshmukhi, Pochampally (M)  
Nalgonda District, Dist-508 284

*[Signature]*

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## Certificate of Participation

This is to Certify that Dr.P Pachiyannan M Designation Professor From

VIITS participated in workshop on "LAB VIEW used in MULTI

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from 25<sup>th</sup> - 30<sup>th</sup> June, 2017.

Coordinator

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*Dr. Pachiyannan M*  
Principal  
Vignan Institute of Technology and Science  
Nalgonda (M), Dist. Nalgonda, Telangana 508284

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## Certificate of Participation

This is to Certify that Mr /Ms A. Jhansi Rani Designation Asst. Professor

From VITS participated in workshop on "LAB VIEW used in MULTI

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from 25<sup>th</sup> - 30<sup>th</sup> June, 2017.

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*S. Srinivas*  
PRINCIPAL  
Vignan Institute of Technology and Science  
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Tel: 9849 590 284

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## Certificate of Participation

This is to Certify that Mr /Ms S.Vamsi Krishna Designation Asst. Professor

From VITS participated in workshop on "LAB VIEW used in MULTI

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from 25<sup>th</sup> - 30<sup>th</sup> June, 2017.

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## Certificate of Participation

This is to Certify that Mr /Ms CH V B Aditya Kumar Designation Asst. Professor

From VTTS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

from 25<sup>th</sup> -30<sup>th</sup> June, 2017.

*Signature*



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Yadadri Bhuvanagiri Dist.-508 284.

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## Certificate of Participation

This is to Certify that Mr /Ms K.Prasad Babu Designation Asst. Professor

From VITS participated in workshop on 'LAB VIEW used in MULTI

DISCIPLINARY' organized by Department of Electronics and Instrumentation Engineering

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## Certificate of Participation

This is to Certify that Mr /Ms K.Prem Sagar Asst. Professor Designation

From VITS participated in workshop on "**LAB VIEW used in MULTI**

**DISCIPLINARY**" organized by Department of Electronics and Instrumentation Engineering

from 25<sup>th</sup> - 30<sup>th</sup> June, 2017.

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Vignan Institute of Technology and Sak  
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(V) Deshmukhi, Pochampally (M)  
Yadadri Bhuvanagiri Dist.-508 28



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## Certificate of Participation

*This is to Certify that Mr /Ms P.Prashanthi Reddy Designation Asst. Professor*

*From VITS participated in workshop on "LAB VIEW used in MULTI*

*DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering*

*from 25<sup>th</sup> - 30<sup>th</sup> June, 2017.*

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## Certificate of Participation

This is to Certify that Mr /Ms P. Sai Kalyan Designation Asst. Professor

From VITS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

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Yadadri Bhuvanagiri

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## Certificate of Participation

This is to Certify that Mr /Ms P.V.P. Kishore Designation Asst. Professor From

VITS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

from 25<sup>th</sup> - 30<sup>th</sup> June, 2017.

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Principal  
Vignan Institute of Technology & Science  
(M) Pochampally (M) Dist: Nalgonda  
508284

Principal



## WORKSHOP REPORT

This report provides a brief summary of the workshop organized by department of Electronics and Instrumentation Engineering and National Instruments, Bangalore. This organized a One-week workshop on "LAB VIEW used in MULTI DISCIPLINARY " on 25<sup>th</sup> - 30<sup>th</sup> June, 2017 from 9.00 am to 3.00 pm in the EIE Seminar Hall, Computer Lab IV<sup>th</sup> Floor, VITS The workshop brought together over 12 Faculty of EIE. The VITS Principal graced the occasion and wished grand success for the program, HOD-EIE.

By National Instruments, we mean the dynamic relationship between learning, teaching and culture. ... Technology interacts with and draws together beliefs about learners and learning, teacher and teaching, and curriculum.

**Resource Persons:** Mr. Vasanth, Application Engineer

Mr. Suneel, Senior Engineer


**Duration:** 6hrs per day

**Topics covered:** Introduction to Lab VIEW  
Examples with Lab VIEW  
Hands on Basics  
Introduction to Graphical system Design  
Platform for Engineering Education using Lab VIEW  
Graphical system design  
Lab VIEW Fundamentals  
NI Multisim for circuits Education  
NI DAQ & Multisim Integration  
NI Vision module Software  
RIO Architecture for Embedded Applications

**Coordinator**

  
Mrs.S.Prasanna



  
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