



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi



Date: 10-05-2017

WORKSHOP PROPOSAL LETTER

To
The Principal,
VITS, Deshmukhi.

Sub:- Request to conduct workshop on "LAB VIEW used in MULTI DISCIPLINARY" for A.Y. 2017-18 and Sanction of budget for the same:-Regd.

Respected Sir,

We are planning to conduct a One week workshop on "LAB VIEW used in MULTI DISCIPLINARY" for academic year 2017-18 by Mr. Vasanth, Application Engineer, and Mr. Suneel, Senior Engineer Bangalore. The Basics of "LAB VIEW used in MULTI DISCIPLINARY" Bangalore workshop is related to Basics of Industry Engineering, Technology trends and Industry requirements of Research, etc. This course will be organized with theory and practical sessions with total duration of 42 hrs for a period of one week from 25th - 30th June, 2017. The total estimated budget for a smooth conduction of this will be 30,000/-

In this regard I request you to grant permission to conduct the same and allocate the requisite budget.

Thanking you,

Yours faithfully,
Coordinator
Mrs. S. Prasanna

Head of the Department
Department of EIE

Expected Amount:

Resource Person Honorarium: 24,000/-
Transport for Resource Person: 1500/-
Flexi: 500/-
Snack Items: 15000/-
Stationery: 1500/-
Memento : 1000/-

PRINCIPAL
Vignan Institute of Technology and Science
(V) Deshmukhi, Pochampally (Mull),
Yadadri Bhuvanagiri Dist.-508 284.





VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi



Date: 10-05-2017

To

Mr. Vasanth,
Application Engineer,
Bangalore.


Sub: Invitation as a Resource person to conduct workshop in our esteemed Organization- Reg

Respected Sir,

We are one of the 8 well reputed engineering institutions under Vignan group of institutions, situated in Deshmukhi about 6km from Ramoji film city. We have B.Tech and M.Tech courses in many branches of engineering, including Electronics & Instrumentation engineering. We have around 500 students including Bachelors and Masters. We would like our faculty to get exposed to industrial environment and more importantly industry practices, which would help them, upon their entry into any industry, to get set in the industry environment faster and deliver their jobs efficiently.

I am Pleased to invite you as a Resource person to conduct workshop in our esteemed organization for the academic year 2017-2018 in your specialized area. This would be a great platform for our faculty to learn from your experiences, and can enhance their knowledge.

With Best Regards,


HOD, EPE
Vignan Institute of Technology and Science
(M) Deshmukhi, Pochampally (MdI),
Yadadri Bhuvanagiri Dist.-508 284.





VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

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Date: 10-05-2017

To

Mr. Suneel,
Senior Engineer,
Bangalore.

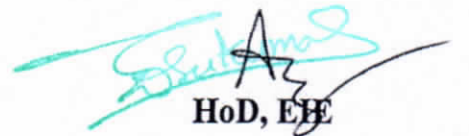
Sub: Invitation as a Resource person to conduct workshop in our esteemed Organization- Reg

Respected Sir,

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I am Pleased to invite you as a Resource person to conduct workshop in our esteemed organization for the academic year 2017-2018 in your specialized area. This would be a great platform for our faculty to learn from your experiences, and can enhance their knowledge.

With Best Regards,


HoD, EIE

PRINCIPAL
Vignan Institute of Technology and Science
(VI) Deshmukhi, Pochampally (Mdt),
Yadadri Bhuvanagiri Dist.-508 284.





EieVits <vits.eie@gmail.com>

INVITATION FOR "LAB VIEW used in MULTI DESCIPLAINARY" PROGRAM

Eie Vits <vits.eie@gmail.com>
To: vasanth425@gmail.com

Wed, May 10 2017 at 11:50 AM

Sir,

It gives me immense pleasure to invite you to our Educational Institution as a Resource Person for a certificate course for Faculty which is scheduled from 25th - 30th June, 2017 on the topic 'LAB VIEW used in MULTI DESCIPLAINARY'. The program begins from time 9:00 am to 5:00pm Your immense and practical experience in the domain is very much useful for the Faculty to develop their skills and they will be fortunate enough to have an illustrious person like you to update and guide them in a right path of their career. Hence, I am looking forward for your presence on our campus and ignite our minds.

Thanking you

Yours faithfully,
Mrs.S.Prasanna
Coordinator

PRINCIPAL
Vignan Institute of Technology and Science
(VI) Deshmukhi, Pochampally (MdI),
Yadadri Bhuvanagiri Dist.-508 284.



EieVits <vits.eie@gmail.com>

INVITATION FOR "LAB VIEW used in MULTI DESCIPLAINARY" PROGRAM

Eie Vits <vits.eie@gmail.com>
To: suneel.kumar@gmail.com

Wed, May 10 2017 at 10:20 AM

Sir,

It gives me immense pleasure to invite you to our Educational Institution as a Resource Person for a certificate course for Faculty which is scheduled from 25th - 30th June, 2017 on the topic '**LAB VIEW used in MULTI DESCIPLAINARY**'. The program begins from time 9:00 am to 5:00pm Your immense and practical experience in the domain is very much useful for the Faculty to develop their skills and they will be fortunate enough to have an illustrious person like you to update and guide them in a right path of their career. Hence, I am looking forward for your presence on our campus and ignite our minds.

Thanking you

Yours faithfully,
Mrs.S.Prasanna
Coordinator



PRINCIPAL
Vignan Institute of Technology and Science
(VI) Deshmukhi, Pochampally (MdI),
Yadadri Bhuvanagiri Dist.-508 284.





Eie Vits <vits.eie@gmail.com>

**ACCEPTANCE OF INVITATION FOR "LAB VIEW used in MULTI DISCIPLINARY"
PROGRAM**

suneel<suneel.kumar@gmail.com>
To: vits.eie@gmail.com

Mon, May 15, 2017 at 5:35 PM

I am glad to a part with an elite educational institution like yours and deliver speech on the "**LAB VIEW used in MULTI DISCIPLINARY**". I delightfully accept your invitation and will make my presence there with the expertise and exposure of the subject.

With regards,

Mr. Suneel,
Senior Engineer,
Bangalore.

PRINCIPAL
Vignan Institute of Technology and Sciences
(M) Deshmukhi, Pochampally (M)
Yadadri Bhuvanagiri Dist.-508 284.

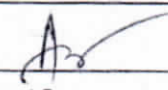




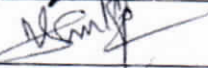


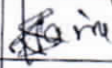
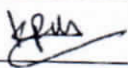

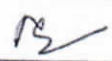


VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING

20th June 2017

CIRCULAR

All the Teaching staff members are here by informed that 25th - 30th June, 2017, our Department is conducting workshop on "LAB VIEW used in MULTI DISCIPLINARY " by Mr. Vasanth, Application Engineer and Mr. Suneel, Senior Engineer National Instrumentation ,Bangalore.

S.No.	Name of the full time teacher	Signature
1	CH V B Aditya Kumar	
2	Dr.Pachiyannan M	
3	K.Padmavathi	
4	K.Prem Sagar	
5	S.Prasanna	
6	P.Prashanthi Reddy	
7	B.Rajesh	
8	A.Jhansi Rani	
9	S.Vamshi Krishna	
10	K.Prasad Babu	
11	PVP Kishore	
12	P.Sai Kalyan	

cc: Principal for kind information,
: All EIE Staff.
: Transport Department


HOD-EIE

PRINCIPAL
Vignan Institute of Technology and Sci-
(VI) Deshmukhi, Pochampally (M)
Yadadri Bhuvanagiri Dist.-508 28





VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

(An ISO 9001 : 2000 Certified Institution)
(Sponsored by Lavu Educational Society)



Program Schedule

DAY1: Introduction to Lab VIEW

Examples with Lab VIEW

Hands on Basics

Day 2: Introduction to Graphical system Design

Platform for Engineering Education using Lab VIEW

Hands on Graphical system design

Day 3: Hands on Training on Lab VIEW Fundamentals

Hands on Training on Lab VIEW Fundamentals

Day 4: Hands on Training on NI Multisim for circuits Education

Hands on Training on NI DAQ & Multisim Integration

Day 5: Hands on Training on NI Vision module Software

Hands on Training on RIO Architecture for Embedded Applications

PRINCIPAL

Vignan Institute of Technology and Sciences
(VI) Deshmukhi, Pechanpally (M.M.)
Yadadri Bhuvanagiri Dist.-508 284.



REGISTRATION FORM

Name.....

Designation.....

Department.....

College Name.....

Address for Communication

.....

.....

.....

Mobile Number.....

Email.....

Payment Details:

Cash: Rs.....

Date.....

.....

Any other information:

.....

.....

.....

.....

.....

Place:

Date: Applicant's Signature

Registration : Important Dates

- > Last date for receiving Applications 14 June 2017.
- > The registration fee is Rs. 300/-per participant and it can be paid by cash. For any clarification the coordinators may be contacted.

Who can participate:

The workshop will be beneficial to the Faculty of all branches to develop industrial applications using PLC and SCADA Software.

Venue:
Vignan Institute of Technology & Science

Contact Persons:

Mrs. S. Prasanna
Assoc. Professor,



PRINCIPAL
Vignan Institute of Technology and Science
(V) Deshmukhi, Pochampally (Mett),
Vadavari Bhuvanagiri Dist - 508284.



A One Week workshop

On

**LAB VIEW used in MULTI
DESCIPLINARY**
25th - 30th June, 2017

Chief Coordinator
Mr. Ch. V.B. Aditya Kumar
HOD, Dept. of EIE, VITS

Coordinator
Mrs.S.Prasanna
Assoc. Professor, Dept. of EIE, VITS

Organized by



**Department of
Electronics and Instrumentation Engineering**

**VIGNAN INSTITUTE OF
TECHNOLOGY AND SCIENCE**
Near Ramoji film City, Deshmukhi (V),
Pochampally(M), Nalgonda District - 508284.
(Affiliated to JNTUH, Accredited by NBA, AICTE)

REGISTRATION FORM

Name..... *K. Prasanna Babu*
Designation..... *Asst. Prof.*
Department..... *E.C.E.*
College Name..... *Sri S. M. J.*
Address for Communication

..... *Sri S. M. J., Hyderabad*
.....
.....

Mobile Number..... *9912992961*
Email..... *prasad_babu@ymail.com*

Payment Details:
Cash: Rs. *3000/-*
Date..... *25/06/17*

Any other information:
.....
.....
.....

Place: *Hyderabad*
Date: *26/06/17*
Applicant's Signature *[Signature]*

Registration : Important Dates
➤ Last date for receiving Applications 14 June 2017.
➤ The registration fee is Rs. 300/-per participant and it can be paid by cash. For any clarification the coordinators may be contacted.

Who can participate:
The workshop will be beneficial to the Faculty of all branches to develop industrial applications using PLC and SCADA Software.

Venue:
Vignan Institute of Technology & Science

Contact Persons:
Mrs. S. Prasanna
Assoc. Professor,

[Handwritten Signature]

PRINCIPAL
Vignan Institute of Technology and Science
(VI) Deshmukhi, Pochampally (Midi),
Yadadri Bhuvanagiri Dist.-508284.



A One Week workshop

On

**LAB VIEW used in MULTI
DISCIPLINARY**
25th - 30th June, 2017

Chief Coordinator
Mr. Ch. V.B. Aditya Kumar
HOD, Dept. of EIE, VITS

Coordinator
Mrs.S.Prasanna
Assoc. Professor, Dept. of EIE, VITS

Organized by



Department of
Electronics and Instrumentation Engineering

**VIGNAN INSTITUTE OF
TECHNOLOGY AND SCIENCE**
Near Ramoji film City, Deshmukhi (V),
Pochampally(M), Nalgonda District - 508284.
(Affiliated to JNTU.H, Accredited by NBA, AICTE)

REGISTRATION FORM

Name..... *K. Padma Lakshmi*
Designation..... *Asst. Prof.*
Department..... *E.T.E.*
College Name..... *C.N.R.C.E.*
Address for Communication
C.N.R., Hyderabad
.....
.....

Mobile Number..... *9775151104*
Email..... *Bengaluru@gmail.com*
Payment Details:
Cash: Rs. *200/-*
Date.....

Any other information:
.....
.....
.....

Place: *Hyd*
Date: _____
Applicant's Signature *[Signature]*

Registration : Important Dates

- > Last date for receiving Applications 14 June 2017.
- > The registration fee is Rs. 300/-per participant and it can be paid by cash. For any clarification the coordinators may be contacted.

Who can participate:

The workshop will be beneficial to the Faculty of all branches to develop industrial applications using PLC and SCADA Software.

Venue:
Vignan Institute of Technology & Science

Contact Persons:

Mrs. S. Prasanna
Assoc. Professor,

[Signature]

PRINCIPAL
Vignan Institute of Technology
(M) Deshmukhi, Pochampally
Yadadri Bhuvangiri Dist 502384



A One Week workshop

On

**LAB VIEW used in MULTI
DESCIPLINARY**
25th - 30th June, 2017

Chief Coordinator
Mr. Ch. V.B. Aditya Kumar
HOD, Dept. of EIE, VITS

Coordinator
Mrs.S.Prasanna
Assoc. Professor, Dept. of EIE, VITS

Organized by



**Department of
Electronics and Instrumentation Engineering**

**VIGNAN INSTITUTE OF
TECHNOLOGY AND SCIENCE**
Near Ramoji film City, Deshmukhi (V),
Pochampally(M), Nalgonda District - 508284.
(Affiliated to JNTUHU, Accredited by NBA, AICTE)

**VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING**

LAB VIEW used in MULTI DISCIPLINARY Workshop Attendance Sheet from 25th - 30th June, 2017

VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE: DESHMUKHI												
DEPARTMENT OF ELECTRONICS INSTRUMENTATION ENGINEERING												
LAB VIEW used in MULTI DISCIPLINARY Workshop Attendance Sheet Day Wise												
S.N	Name of the staff	College Name	25/06/2017	26/06/2017	27/06/2017	28/06/2017	29/06/2017	30/06/2017				
1	CH V B Aditya Kumar	VITS	Att	Att	Att	Att	Att	Att				
2	Dr. N.V.R Vikarm G	VITS	mbg	mbg	mbg	mbg	mbg	mbg				
3	K.Bhavani	VITS	K.Bhavani	K.Bhavani	K.Bhavani	K.Bhavani	K.Bhavani	K.Bhavani				
4	K.Padmavathi	VITS	VP	VP	VP	VP	VP	VP				
5	K.Prem Sagar	VITS	VP	VP	VP	VP	VP	VP				
6	S.Prasanna	VITS	S	S	S	S	S	S				
7	P.Prashanthi Reddy	VITS	VP	VP	VP	VP	VP	VP				
8	B.Rajesh	VITS	B	B	B	B	B	B				
9	CH Shanthi Priya	VITS	SP	SP	SP	SP	SP	SP				
10	A.Jhansi Rani	VITS	AR	AR	AR	AR	AR	AR				
11	S.Vamshi Krishna	VITS	SK	SK	SK	SK	SK	SK				
12	P.Anitha	VITS	AN	AN	AN	AN	AN	AN				
13	K. Prasad Babu	Greehan University	KPB	KPB	KPB	KPB	KPB	KPB				
14	K. Prudhwalakshmi	CVR	KP	KP	KP	KP	KP	KP				
15	D. Santhosh	Critan	DS	DS	DS	DS	DS	DS				



Sankumar
PRINCIPAL
Vignan Institute of Technology and Science
Institute of Technology (MHT)
Deshmukhi, Pochampally (MHT)
Dr. Prudhwalakshmi, Pochampally Dist.-508 284.

VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Department of Electronics and Instrumentation Engineering

WORKSHOP/FDP FEEDBACK

Name of the Faculty: K. Prasad Babu

Date: 30-06-17

Name of the Workshop/FDP: Lab uses used in multidisciplinary

College Name: Gitam's

Please agree/disagree with the following statements:

	Excellent	Good	Average	Poor	Remarks
The workshop facilities were comfortable and appropriate.	/				
Contents of the modules		/			
Aspects of program to bridge the gap between theory and application	/				
The length of the workshop was right.	/				
The workshop materials provided were appropriate and helpful		/			
Time management		/			
The instructor was knowledgeable in the subject area	/				
The hands-on exercises supported the lecture and discussion		/			
Overall quality of program	/				

K. Prasad Babu
Signature of the Faculty
Principal
Vignan Institute of Technology and Science
(VI) Dasmukhi, Pochampally (N.C.II),
Yadadri Bhuvanagiri Dist.-508 211



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Department of Electronics and Instrumentation Engineering

WORKSHOP/FDP FEEDBACK

Name of the Faculty: *D. Satish*

Date: *30-06-17*

Name of the Workshop/FDP: *Lab view and PLC Multidisciplinary*

College Name: VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Please agree/disagree with the following statements:

	Excellent	Good	Average	Poor	Remarks
The workshop facilities were comfortable and appropriate.	✓				
Contents of the modules	✓				
Aspects of program to bridge the gap between theory and application		✓			
The length of the workshop was right.		✓			
The workshop materials provided were appropriate and helpful	✓				
Time management			✓		
The instructor was knowledgeable in the subject area	✓				
The hands-on exercises supported the lecture and discussion		✓			
Overall quality of program		✓			

Satish
Signature of the Faculty

S. Subramanian
PRINCIPAL

Vignan Institute of Technology and Science
(VI) Deshmukhi, Pochampally (Md),
Yadadri Bhuvanagiri Dist.-508 284.





VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Vignan Hills, Deshmukhi(V), Pochampally(M), near Ramoji Film City Nalgonda(dist)-508284

Certificate of Participation

This is to Certify that Mr /Ms K.Prasad Babu Designation Asst. Professor

From VITS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

from 25th - 30th June, 2017.



PRINCIPAL
Vignan Institute of Technology and Sciences
(V) Deshmukhi, Pochampally (Mdl),
Yadadri Bhuvanagiri Dist.-508 284.

Coordinator

HOD

Principal



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Vignan Hills, Deshmukhi(V), Pochampally(M), near Ramoji Film City Nalgonda(dist)-508284

Certificate of Participation

This is to Certify that Mr /Ms K.Prem Sagar Designation Asst. Professor

From VITS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

from 25th - 30th June, 2017.



Coordinator

PRINCIPAL
Vignan Institute of Technology and Science
(M) Deshmukhi, Pochampally (Mcd),
Yadaadri Bhuvanagiri Dist.-508 284.

HOD

Principal



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Vignan Hills, Deshmukhi(V), Pochampally(M), near Ramoji Film City Nalgonda(dist)-508284

Certificate of Participation

This is to Certify that Mr /Ms P.Prashanthi Reddy Designation Asst. Professor

From VITS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

from 25th - 30th June, 2017.

S. Kumar



PRINCIPAL
Vignan Institute of Technology and Science
(V) Deshmukhi, Pochampally (M),
Yadadri Bhuvanagiri Dist.-508 284.

[Signature]

HOD

Principal

[Signature]

Coordinator



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Vignan Hills, Deshmukhi(V), Pochampally(M), near Ramoji Film City Nalgonda(dist)-508284

Certificate of Participation

This is to Certify that Mr /Ms P. Sai Kalyan Designation Asst. Professor

From VITS participated in workshop on 'LAB VIEW used in MULTI

DISCIPLINARY' organized by Department of Electronics and Instrumentation Engineering

from 25th - 30th June, 2017.

S. Kumar

PRINCIPAL
Vignan Institute of Technology and Science
(V) Deshmukhi, Pochampally (Md),
Yadadri Bhuvanagiri Dist.-508 284.



[Signature]

Coordinator

[Signature]

HOD

Principal



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Vignan Hills, Deshmukhi(V), Pochampally(M), near Ramoji Film City Nalgonda(dist)-508284

Certificate of Participation

This is to Certify that Mr /Ms P.V.P. Kishore Designation Asst. Professor From

VITS participated in workshop on "LAB VIEW used in MULTI

DISCIPLINARY" organized by Department of Electronics and Instrumentation Engineering

from 25th - 30th June, 2017.

Coordinator



HOD

PRINCIPAL

Vignan Institute of Technology and Science,
(M) Deshmukhi, Pochampally (Nidhi),
Yadadri Bhuvanagiri Dist.-508 284.

Principal



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi



WORKSHOP REPORT

This report provides a brief summary of the workshop organized by department of Electronics and Instrumentation Engineering and National Instruments, Bangalore. This organized a One-week workshop on "LAB VIEW used in MULTI DESCIPLAINARY " on 25th - 30th June, 2017 from 9.00 am to 3.00 pm in the EIE Seminar Hall, Computer Lab IVth Floor, VITS The workshop brought together over 12 Faculty of EIE. The VITS Principal graced the occasion and wished grand success for the program, HOD-EIE.

By National Instruments, we mean the dynamic relationship between learning, teaching and culture. ... Technology interacts with and draws together beliefs about learners and learning, teacher and teaching, and curriculum.

Resource Persons: Mr. Vasanth, Application Engineer

Mr. Suneel, Senior Engineer

Duration: 6hrs per day

Topics covered: Introduction to Lab VIEW
Examples with Lab VIEW
Hands on Basics
Introduction to Graphical system Design
Platform for Engineering Education using Lab VIEW
Graphical system design
Lab VIEW Fundamentals
NI Multisim for circuits Education
NI DAQ & Multisim Integration
NI Vision module Software
RIO Architecture for Embedded Applications

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(VI) Deshmukhi, Pochampally (M),
Yadadri Bhuvanagiri Dist.-508 284.

Coordinator

Mrs.S.Prasanna





2017-18
ECE

WORKSHOP PROPOSAL LETTER

Date: 17th July, 2017

To
 The HOD
 Department of ECE
 VITS, Deshmukhi.

Sub:- Request to conduct workshop for A.Y. 2017-18 and
 Sanction of budget for the same:-Regd.

Respected Sir,

We are planning to conduct the a one workshop on "Recent Trends in VLSI & Embedded System" for academic year 2017-18 by Experts from National Instruments, Bangalore and Embedded Systems Solutions Pvt Ltd, Bangalore. The Recent Trends in VLSI & Embedded System workshop is Research trends in the areas of VLSI & ES, for the benefit of faculty & students, under TEQIP-II. This course will be organized with theory and practical sessions with total duration of 30hrs for a period of one week from 11-12-2017 to 16-12-2017. The total estimated budget for a smooth conduction of this will be 47,200/-

In this regard I request you to grant permission to conduct the same and allocate the requisite budget.

Thanking you,

Yours faithfully,
 Coordinator

[Signature]
 Dr. J. V. Rao

*ok
 Teachers*

Expected Amount:

- Resource Person Honorarium: 41,000/-
- Transport for Resource Person: 2500/-
- Flexi: 300/-
- Snack Items: 1400/-
- Stationery: 750/-
- Momento : 1000/-

*To the Principal
 forwarded
 sanction for workshop*



[Signature]

PRINCIPAL
 Vignani Institute of Technology and Science
 Deshmukhi, Bhongir Yadadri Dist-508 284



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi

EAMCET CODE : VGNT



CIRCULAR

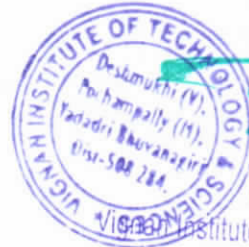
19th Nov 2017

All the Teaching staff here by informed that on 11th to 16th Dec. 2017, Department of ECE is conducting a One Week Workshop on "Recent Trends in VLSI & Embedded System" by JNTUH, Industry and R & D centers like RCI, C-DAC and RedPine Systems.

HOD-ECE

cc: Principal for kind information,
All ECE Staff.
Transport Department

S.N	Name of the Faculty	Signature
1	Dr.Nali Dinesh Kumar	
2	Mrs.PydimarriPadmaja	
3	Mrs.SwapnaRevuri	
4	Mrs. R B Kalyani	
5	Ms. Y. Jyothi	
6	K.MuraliChandaraBabu	
7	Mr.VeguruPrakasam	
8	Ms.Vani A	
9	Ms.D. Swarupa Rani	
10	Mr. B. VIJAY KUMAR	
11	Mr.B.Madhusudhan Reddy	
12	Mr. Ch. Vamshi Krishna	
13	Mr. D. Srinivas	
14	Mr. G. Mahender	
15	Mr. K R AnudeepLaxmikanth	



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| 1 | Dr. N. Dinesh Kumar, | Chairman |
| | HOD | |
| 2 | Mr.P. Nageswaro | Coordinator |
| 3 | Dr. J. V Rao | Coordinator |
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| 5 | Mrs.P.Padmaja | Member |
| 6 | Mrs. Harshavardhini | Member |
| 7 | Ms. Vijayalaxmi | Member |

A

**One
WORKSHOP
On**

**Recent Trends in VLSI & Embedded
System**

11th - 16th December, 2017

Organized by
**Department of Electronics
&**

Communication Engineering



**VIGNAN INSTITUTE OF
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**Department of Electronics and Communication
Engineering**



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Participant's personal Details:

Name of the Faculty:

(in Block letters):

Mobile:

Email:

Participant's Institute's Details:

Name:

Reg.No:

Telephone No:

Department:

Payment Details: Cash/DD

Cash/ Cheque/ Draft (please specify):

Cheque/ Draft details (if applicable):

Signature of Candidate with date

Signature of College Official with date

Samyukta



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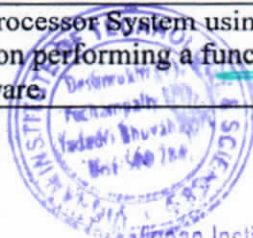
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Recent Trends in VLSI & Embedded System Workshop Program Schedule Day Wise

Day	Time	Topics to be covered
Day 1 11/12/17	09:30AM - 10:15AM	Inauguration
	10:15AM - 11:15AM	7-Series Architecture Overview
	11:15AM - 11:30AM	Tea/Coffee Break
	11:30AM - 1:00 PM	7-Series Architecture Overview
	1:00PM - 2:00PM	Lunch Break
	2:00PM - 3:30PM	Vivado Design Flow
	3:30PM - 4:30PM	Tea/Coffee Break
	4:30PM - 5:30PM	General Discussion
Day 2 12/12/17	09:30AM - 11:00AM	Vivado Design Flow
	11:00AM - 11:15AM	Lab 1: Creating an HDL Design
	11:15AM - 11:30AM	Tea/Coffee Break
	11:30AM - 1:00 PM	Lab 1: Creating an HDL Design
	1:00PM - 2:00PM	Lunch Break
	2:00PM - 3:30PM	Lab 2: Xilinx Design Constraints
	3:30PM - 4:30PM	Tea/Coffee Break
	4:30PM - 5:30PM	Lab 2: Xilinx Design Constraints
Day 3 13/12/17	09:30AM - 11:00AM	IP Integrator and Embedded System Design Flow
	11:00AM - 11:15AM	IP Integrator and Embedded System Design Flow
	11:15AM - 11:30AM	Tea/Coffee Break
	11:30AM - 1:00 PM	Lab 3: Create a Processor System using IP Integrator
	1:00PM - 2:00PM	Lunch Break
	2:00PM - 3:30PM	Lab 3: Create a Processor System using IP Integrator
	3:30PM - 4:30PM	Tea/Coffee Break
	4:30PM - 5:30PM	General Discussion
Day 4 14/12/17	09:30AM - 11:00AM	Embedded System Design with Custom IP
		Lab 4: Creating and Adding Your Own Custom IP: Use the Manage IP feature of Vivado to create a custom IP and extend the system with the custom peripheral.
	11:00AM - 11:15AM	Write a basic C application to access the peripherals.
	11:15AM - 11:30AM	Tea/Coffee Break
	11:30AM - 1:00 PM	Lab 4: Creating and Adding Your Own Custom IP: System Debugging using Vivado Logic Analyzer and SDK
	1:00PM - 2:00PM	Lunch Break
		Lab 5: Debugging using Vivado Logic Analyzer cores <ul style="list-style-type: none"> ➤ Insert various Vivado Logic Analyzer cores to debug/analyze system behavior. ➤ Profiling and Performance Improvement ➤ Introduction to High-Level Synthesis with Vivado HLS ➤ Improving Performance and Resource Utilization ➤ Creating an Accelerator
	2:00PM - 3:30PM	
3:30PM - 4:30PM	Tea/Coffee Break	
	Lab 6: Creating a Processor System using Accelerator <ul style="list-style-type: none"> ➤ Profile an application performing a function both in software and hardware. 	
4:30PM - 5:30PM		



S. S. S. S.

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		<ul style="list-style-type: none"> ➤ Create an accelerator in Vivado HLS. ➤ Use the generated accelerator to build a complete system
Day 5 15/12/17	09:30AM - 11:00AM	Fundamentals of VLSI chip design methodology ➤ Basics of ASIC & FPGA
	11:00AM - 11:15AM	Fundamentals of VLSI chip design methodology ➤ Concepts of HDL design and verification for front end
	11:15AM - 11:30AM	Tea/Coffee Break
	11:30AM - 1:00 PM	Fundamentals of VLSI chip design methodology ➤ Synthesis technique overview w.r.t. ASIC & FPGA
	1:00PM - 2:00PM	Lunch Break
	2:00PM - 3:30PM	Fundamentals of VLSI chip design methodology ➤ Details of ASIC foundry & technology
	3:30PM - 4:30PM	Tea/Coffee Break
	4:30PM - 5:30PM	Fundamentals of VLSI chip design methodology ➤ Concepts of DFT and DFT verification
Day 6 16/12/17	09:30AM - 11:00AM	Lab on HDL Design flow for front end ➤ HDL design, verification & synthesis ➤ Understanding netlists for ASIC & FPGA ➤ Hands on Session
	11:00AM - 11:15AM	
	11:15AM - 11:30AM	Tea/Coffee Break
	11:30AM - 1:00 PM	➤ Concepts of ASIC Full custom & Semi-custom methodologies ➤ Understanding Back end design with Layout design flow ➤ Concepts of SDL & RTL to GDSII
	1:00PM - 2:00PM	Lunch Break
	2:00PM - 3:30PM	➤ Calculating Area, Timing & Power & optimizing ➤ Layout verification methodologies (DRC,LVS,PEX,DFM,PERC) ➤ Post-layout verification or Back annotation & exporting to GDSII ➤ Concepts of DFT
	3:30PM - 4:30PM	Tea/Coffee Break
	4:30PM - 5:30PM	Closing Ceremony
Thank You		

Coordinator

Dr. J. V. Rao

Mrs. P.A. Harsha Vardhini



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Deshmukhi, Bhongir Yadadri Dist-508 284



vits ece <ecevgnt89@gmail.com>

Workshop On Recent Trends in VLSI & Embedded System

1 message

vits ece <ecevgnt89@gmail.com>
To: masharani73@gmail.com

14 September 2017 at 13:21

Sub: Invitation as a Visiting Faculty to deliver Lecture in our esteemed Organization. Reg

Respected Madam,

We are one of the 8 well reputed engineering institutions under Vignan group of institutions, situated in Deshmukhi about 6km from Ramoji film city. We have B.Tech and M.Tech courses in many branches of engineering, including Electronics & communication engineering. We have around 500 students including Bachelors and Masters. We would like our faculty to get exposed to industrial environment and more importantly industry practices, which would help them, upon their entry into any industry, to get set in the industry environment faster and deliver their jobs efficiently.

I am Pleased to invite you as a Visiting Faculty to deliver a series of Lecture in our esteemed organization for the academic year 2017-2018 in your specialized area. This would be a great platform for our faculty to learn from your experiences, and can enhance their knowledge.


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2/25/2019

Gmail - Hello Sir/Madam



vits ece <ecevnt89@gmail.com>

Hello Sir/Madam

1 message

masharani73@gmail.com <masharani73@gmail.com>
To: vits ece <ecevnt89@gmail.com>

16 September 2017 at 16:36

Hello Sir/Madam,
Thank you for invitation.

Required audio-visual equipment : computer/laptop, Projector, Wi-Fi is required.

Thank you for giving me such a great opportunity.

Regards,

Dr. M. Asha Rani

Professor & Head Department of ECE, JNTUH CEH



Satish Kumar
PRINCIPAL

VIT Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284

Web : www.jntuh.ac.in
E Mail : pa2registrar@jntuh.ac.in
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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
(Established by Govt. Act No. 30 of 2008)
Kukatpally, Hyderabad - 500 085, Telangana, India



Dr. M AshaRani

PhD in Faculty of ECE. M.Tech Digital Systems & Computer Electronics. B.E ECE

Professor

Electronics & Communication Engineering

Submitted Ph.D Thesis on "Design of Built in self-Test and repair scheme for static RAMs".
Published 6 technical papers in National & International Conferences. Co-ordinated one TAPTEC project of Rs.6.50 Lakhs in establishing VLSI Lab and one R&D project of Rs. 9.0 Lakhs in designing an ASIP for Image Processing applications sponsored by AICTE, Delhi, Co-ordinator for SPOORTHI' 04 a student symposium. Co-ordinated 4 Refresher Courses conducted by Acad. Staff College, JNTU Hyderabad. Attended about 10 workshops on VLSI -EDA Tools, Design for Testability, Altera-Tools, SILVACO tools, National Instruments - Virtual Instruments - Lab view etc.

Areas of Interest: Digital System Design, Design of Fault Tolerant Systems, Microprocessor & Microcontrollers, VLSI Design & Embedded Systems.



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Recent Trends in VLSI & Embedded System Workshop Attendance Sheet

S. N	Name of the Faculty	College Name	11/12/17	12/12/17	13/12/17	14/12/17	15/12/17	16/12/17
1	Dr.Nali Dinesh Kumar	VITS	<i>DK</i>	<i>DK</i>	<i>DK</i>	<i>DK</i>	<i>DK</i>	<i>DK</i>
2	Mrs.PydimarriPadmaja	VITS	<i>PPadma</i>	<i>PPadma</i>	<i>PPadma</i>	<i>PPadma</i>	<i>PPadma</i>	<i>PPadma</i>
3	Mrs.SwapnaRevuri	VITS	<i>Swapna</i>	<i>Swapna</i>	<i>Swapna</i>	<i>Swapna</i>	<i>Swapna</i>	<i>Swapna</i>
4	Mrs. R B Kalyani	VITS	<i>Kalyani</i>	<i>Kalyani</i>	<i>Kalyani</i>	<i>Kalyani</i>	<i>Kalyani</i>	<i>Kalyani</i>
5	Ms. Y. Jyothi	VITS	<i>Jyothi</i>	<i>Jyothi</i>	<i>Jyothi</i>	<i>Jyothi</i>	<i>Jyothi</i>	<i>Jyothi</i>
6	K.MuraliChandaraBabu	VITS	<i>Murali</i>	<i>Murali</i>	<i>Murali</i>	<i>Murali</i>	<i>Murali</i>	<i>Murali</i>
7	Mr.VeguruPrakasam	VITS	<i>VPras</i>	<i>VPras</i>	<i>VPras</i>	<i>VPras</i>	<i>VPras</i>	<i>VPras</i>
8	Ms.Vani A	VITS	<i>VA</i>	<i>VA</i>	<i>VA</i>	<i>VA</i>	<i>VA</i>	<i>VA</i>
9	Ms.D. Swarupa Rani	VITS	<i>DSwar</i>	<i>DSwar</i>	<i>DSwar</i>	<i>DSwar</i>	<i>DSwar</i>	<i>DSwar</i>
10	Mr. B. VIJAY KUMAR	VITS	<i>BVK</i>	<i>BVK</i>	<i>BVK</i>	<i>BVK</i>	<i>BVK</i>	<i>BVK</i>
11	Mr.B.Madhusudhan Reddy	VITS	<i>MB</i>	<i>MB</i>	<i>MB</i>	<i>MB</i>	<i>MB</i>	<i>MB</i>
12	Mr. Ch. Vamshi Krishna	VITS	<i>Vamshi</i>	<i>Vamshi</i>	<i>Vamshi</i>	<i>Vamshi</i>	<i>Vamshi</i>	<i>Vamshi</i>
13	Mr. D. Srinivas	VITS	<i>DS</i>	<i>DS</i>	<i>DS</i>	<i>DS</i>	<i>DS</i>	<i>DS</i>
14	Mr. G. Mahender	VITS	<i>GM</i>	<i>GM</i>	<i>GM</i>	<i>GM</i>	<i>GM</i>	<i>GM</i>
15	Mr. K R AnudeepLaxmikant h	VITS	<i>Anudeep</i>	<i>Anudeep</i>	<i>Anudeep</i>	<i>Anudeep</i>	<i>Anudeep</i>	<i>Anudeep</i>
16	M. RamanaRao	SITS	<i>MR</i>	<i>MR</i>	<i>MR</i>	<i>MR</i>	<i>MR</i>	<i>MR</i>
17	P. Chandra sekar	SITS	<i>Chandra</i>	<i>Chandra</i>	<i>Chandra</i>	<i>Chandra</i>	<i>Chandra</i>	<i>Chandra</i>
18	D. Mohan Kumar	TKR	<i>Mohan</i>	<i>Mohan</i>	<i>Mohan</i>	<i>Mohan</i>	<i>Mohan</i>	<i>Mohan</i>
19	B. Choudari Kumar	MLR	<i>Kumar</i>	<i>Kumar</i>	<i>Kumar</i>	<i>Kumar</i>	<i>Kumar</i>	<i>Kumar</i>
20	N. RamaKrishna	MLR	<i>NRK</i>	<i>NRK</i>	<i>NRK</i>	<i>NRK</i>	<i>NRK</i>	<i>NRK</i>
21	P. Kishore	JBIT	<i>Kish</i>	<i>Kish</i>	<i>Kish</i>	<i>Kish</i>	<i>Kish</i>	<i>Kish</i>
22	B. Shudeer	VBIT	<i>BS</i>	<i>BS</i>	<i>BS</i>	<i>BS</i>	<i>BS</i>	<i>BS</i>
23	G. Gangadhar	VBIT	<i>GG</i>	<i>GG</i>	<i>GG</i>	<i>GG</i>	<i>GG</i>	<i>GG</i>
24	A.Ragu Veer	CVR	<i>Ragu</i>	<i>Ragu</i>	<i>Ragu</i>	<i>Ragu</i>	<i>Ragu</i>	<i>Ragu</i>
25								
26								
27								
28								



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Deshmukhi, Bhongir Yadadri Dist-508 284

Coordinator

Dr. J. V. Rao

VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE
 Department of Electronics and Communication Engineering



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 On

Recent Trends in VLSI & Embedded System

11th - 16th December, 2017

Participant's personal Details:
 Name of the Faculty: Dr.Nali Dinesh Kumar
 Mobile: 9440076901
 Email: dimuhai@yahoo.com

Participant's Institute's Details:
 Name: **VITS**
 Reg.No.:
 Telephone No. **9866399664**
 Department **ECE**
 Payment Details: **Cash/DD**
 Cash/ Cheque/ Draft (please specify)
 Cheque/ Draft details (if applicable)

Signature of Candidate with date



Signature of College Official with date
 Vignan Institute of Technology and Science
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 Dr. G. Durga Srinumath
 Principal

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| 3 | Dr. J. V Rao | Coordinator |
| 4 | Mr.V.Prakasam | Member |
| 5 | Mrs.P.Padmaja | Member |
| 6 | Mrs.Harshavardhini | Member |
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Certificate of Participation

This is to certify that Dr. Nali Dinesh Kumar from *Department of*


Electronics & Communication Engineering of Vignan Institute of Technology & Science has participated in
Program on "Recent Trends in VLSI & Embedded System" organized by Department of Electronics & Communication
Engineering from 11th - 16th December, 2017.


Coordinator





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Institute of Technology and Science
Deshmukhi, Bongir Yadadri Dist-508 284


Principal



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EAMCET CODE : VGNT

WORKSHOP/FDP FEEDBACK

Name of the Faculty: Dr. Nali Dinesh Kumar Date: 16th December, 2017

Name of the Workshop/FDP: Recent Trends in VLSI & Embedded system

College Name: VITS

Please agree/disagree with the following statements:

	Excellent	Good	Average	Poor	Remarks
The workshop facilities were comfortable and appropriate.	✓				
Contents of the modules	✓				
Aspects of program to bridge the gap between theory and application		✓			
The length of the workshop was right.	✓				
The workshop materials provided were appropriate and helpful	✓				
Time management		✓			
The instructor was knowledgeable in the subject area		✓			
The hands-on exercises supported the lecture and discussion		✓			
Overall quality of program		✓			



[Handwritten Signature]

[Handwritten Signature]
Signature of the Faculty

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near Ramoji Film City,
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| | HOD | |
| 2 | Mr. P. Nageswaro | Coordinator |
| 3 | Dr. J. V Rao | Coordinator |
| 4 | Mr. V. Prakasam | Member |
| 5 | Mrs. P. Padmaja | Member |
| 6 | Mrs. Harshavardhini | Member |
| 7 | Ms. Vijayalaxmi | Member |

**VIGNAN INSTITUTE OF TECHNOLOGY &
SCIENCE**
Department of Electronics and Communication
Engineering



**REGISTRATION FORM
WORKSHOP**

On

Recent Trends in VLSI & Embedded

System

11th - 16th December, 2017

Participant's personal Details:

Name of the Faculty: Mrs. Pydimarri Padmaja

Mobile: 9963698646

Email: padmaja@gmail.com

Participant's Institute's Details:

Name: N.I.T.S

Reg.No:

Telephone No: 9866399664

Department: E.C.E

Payment Details: Cash/DD

Cash/ Cheque/ Draft (please specify):

Cheque/ Draft details (if applicable):

Signature of Candidate with date:

Signature of Faculty: *P. Padmaja*

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Deshmukhi, Bhongir, Yadadri Dist-508 284



Dr. G. Durga Sukumar

VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE
Deshmukhi, Bhongir, Yadadri Dist-508 284



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Vignan Hills, Near Ramoji Film City, Deshmukhi Vill., Pochampally Mdl., Yadadri Bhuvanagiri Dist. - 508 284. I.S.



Certificate of Participation

This is to certify that Mrs. Pydimarri Padmaja from Department of

Electronics & Communication Engineering of Vignan Institute of Technology & Science has participated in Program on "Recent Trends in VLSI & Embedded System" organized by Department of Electronics & Communication Engineering from 11th - 16th December, 2017.



Sukumar

PRINCIPAL
Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284

[Signature]

Coordinator

[Signature]

Head of the Department

[Signature]

Principal



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi

EAMCET CODE : VGNT



WORKSHOP/FDP FEEDBACK

Name of the Faculty: Mrs. Pydimarri padmaja Date: 16th December, 2017

Name of the Workshop/FDP: Recent trends in VLSI & Embedded system

College Name: VITS

Please agree/disagree with the following statements:

	Excellent	Good	Average	Poor	Remarks
The workshop facilities were comfortable and appropriate.	✓				
Contents of the modules		✓			
Aspects of program to bridge the gap between theory and application	✓				
The length of the workshop was right.		✓			
The workshop materials provided were appropriate and helpful	✓	✓			
Time management	✓				
The instructor was knowledgeable in the subject area		✓			
The hands-on exercises supported the lecture and discussion		✓			
Overall quality of program	✓				



PRINCIPAL

Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284

Subarna

P. Panay

Signature of the Faculty



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

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EAMCET CODE : VGNT



WORKSHOP REPORT

This report provides a brief summary of the Workshop organized by department of Electronics and Communication Engineering. The workshop is on "Recent Trends in VLSI & Embedded System". It is a one week workshop from date 11th - 16th December, 2017, from 9.30 am to 5.30 pm in the Seminar Hall, 1st Floor, VITS. The workshop brought together over 15 Faculty of ECE. The VITS Principal graced the occasion and wished grand success for the program, HOD-ECE.

On the first day, Dr. M. Asha Rani focused on the fundamentals of VLSI chip design methodology along with the concepts of ASIC full-custom and semi-custom design, which was followed by hands-on on Mentor Graphics tool for Simulation and Layout design. On the second day, he briefed the participants about the 7-Series Architecture and the basics of Vivado design flow, which was followed by hands-on on creating HDL and Embedded system design flow and its constraints. On the last day of the workshop, the resource person focused on debugging of system design using Vivado logic analyzer and SDK, and introduced high-level synthesis with Vivado HLS.

Resource Person: Dr. M. Asha Rani, Professor & Head Department of ECE, JNTUH CEH

Duration: 6 hrs per day

Topics covered:

7-Series Architecture Overview, Vivado Design Flow, Lab 1: Creating an HDL Design, Lab 2: Xilinx Design Constraints, IP Integrator and Embedded System Design Flow, Lab 3: Create a Processor System using IP Integrator, Embedded System Design with Custom IP & Debugging using Vivado Logic Analyzer and SDK, Lab 4: Debugging using Vivado Logic Analyzer cores, Introduction to High-Level Synthesis with Vivado HLS, Lab 5: Creating a Processor System using Accelerator, Lab 6: Hardware Co-simulation of FIR Filter using, Xilinx Vivado System Generator, Fundamentals of VLSI chip design Methodologies, Lab 1: HDL flow for front end Concepts of ASIC Full custom & Semi custom, Lab 2: RTL to GDSII using semicustom design flow, Lab 3 : ASIC Full custom design & verification, Concepts & Demo on Analog Mixed design & verification.



Ashu Kumar

PRINCIPAL

Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

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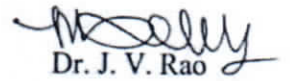
EAMCET CODE : VGNT



Photo gallery:



Coordinator


Dr. J. V. Rao



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Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

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EAMCET CODE : VGNT



Administrative Training Program Proposal Letter

Date: 06/06/17

To

The HOD

VITS, Deshmukhi.

Sub:- Request to conduct workshop for A.Y. 2017-18 and

Sanction of budget for the same:-Regd.

Respected Sir,

We are planning to conduct an administrative training program on "WORKSHOP ON MATLAB" for academic year 2017-18 by Prof. M. Harikrishna, Professor, Dept of ECE, BITS, Kurnool. This course will be organized with theory and practical sessions with total duration of 25 hrs for a period of 5 days from 04-06-2018 to 08-06-2018. The total estimated budget for a smooth conduction of this will be 22,985/-

In this regard I request you to grant permission to conduct the same and allocate the requisite budget.

Thanking you,

Yours faithfully,

Coordinator

Mrs. V. Prakasam

Expected Amount:

1. Resource Person Honorarium: 20,000/-
2. Transport for Resource Person: 1200/-
3. Flexi: 250/-
4. Snack Items: 475/-
5. Stationery: 420/-
6. Memento : 640/-



Principal Sir,
per the discussions
in HOD meeting, I am
forwarding the budget
for the workshop to be
organised for Non-teaching staff

Prakasham
PRINCIPAL

Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284



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EAMCET CODE : VGNT



CIRCULAR

24th May 2018

All the Non-Teaching staff here by informed that on 4th June, 2018 Department of ECE is conducting afivedays workshop on "MATLAB" by Professor, BITS Pilani, Kurnool.


HOD-ECE

cc: Principal for kind information,
All ECE Staff.
Transport Department




PRINCIPAL

Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284

WORKSHOP

On

MATLAB

[4th-8th, June, 2018]

Organized by

**Department of Electronics
&**

Communication Engineering



**VIGNAN INSTITUTE OF
TECHNOLOGY & SCIENCE**
Deshmukhi(V), Pochampally(M),
near Ramoji Film City
Nalgonda(dist)-508284

www.vignanits.ac.in

INTRODUCTION & OBJECTIVE

MATLAB (matrix laboratory) is a multi-paradigm numerical computing environment and proprietary programming language developed by Math Works. MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages, including C, C++, C#, Java, Fortran and Python.

Although MATLAB is intended primarily for numerical computing, an optional toolbox uses the MuPAD symbolic engine, allowing access to symbolic computing abilities. An additional package, Simulink, adds graphical multi-domain simulation and model-based design for dynamic and embedded systems.

As of 2018, MATLAB has more than 3 million users worldwide. MATLAB users come from various backgrounds of engineering, science, and economics.

About the Workshop:

The entire workshop is designed to match the experimental learning approach. Every participant is highly engaged in all hand-worked numerical calculation and programming. Each session comprises of introduction to theoretical and mathematical background, followed by MATLAB exercises. It is roughly expected around 35-40 exercises will be explored in 2 days, with minimal /nil usage of built-in functions..

Intended Participants: The workshop is mainly organized for faculty members & students from academics & industry in field of ECE, EIE, EEE, and ME.

Course Highlights

- Signal Processing Techniques
- Introduction to MATLAB, Hands on using Signal Processing Toolbox
- FIR & IIR Filter Design
- Multirate Signal Processing
- Digital Image Processing-Image Enhancement,
- Segmentation, Restoration & Morphological
- Operations
- Advanced DIP
- Signal Processing- Research point of view
- DIP hands on using MATLAB
- Basics of Wireless Communications & of Cellular
- Network
- 3G Overview, OFDM
- LTE, LTE Architecture
- Requirements in 5G, 5G Architecture, Issues in 5G
- Signal Processing Requirements for 5G
- Hands on using Communication Toolbox

RESOURCE PERSONS:

1. Prof. M. Harikrishna, Professor,
Dept of ECE, BITS, Kurnool



Deshmukhi, Bheerappa
Dist-508 284

INSTITUTE VISION AND MISSION

Vision:

To evolve into a center of excellence in Science & Technology through creative and innovative practices in teaching-learning, promoting academic achievement & research excellence to produce internationally accepted competitive and world class professionals who are psychologically strong and emotionally balanced imbued with social consciousness and ethical values.

Mission:

To provide high quality academic programmes, training activities, research facilities and opportunities supported by continuous industry - institute interaction aimed at employability, entrepreneurship, leadership and research aptitude among students and contribute to the economic and technological development of the region, state and nation.

ECE Department Vision

To impart technical education through latest pedagogical techniques and transform students into competent, committed & creative professionals, with abilities to address the challenges of the century for the benefit of society by imparting high quality education with human values and to emerge as a premier center for electronics & communication engineering.

ECE Department Mission

- To provide high quality academic programmes, training activities and research facilities to the students of Electronics and Communication Engineering in the related field.
- To develop capabilities and skillful in undertaking the core designs of Electronics and Communication Engineering supported by continuous industry - institute interaction aimed at employability.
- To provide leadership and research aptitude among students of electronics and Communication Engineering.
- To inculcate the exploration skills among the students for the contribution of economic and technological development in national and as well as global arena.

VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Department of Electronics and Communication Engineering



MOTTO:
This VIT's motto is to become "Excellence through Dedication".

OUR PHILOSOPHY:
To provide high quality engineering education, VITS recognizes that good faculty, library resources and laboratory and infrastructure are the essential components, and therefore, it is making all efforts to provide these resources.

REGISTRATION FORM

WORKSHOP

ON

MATLAB[4th-8th, June, 2018]

Participant's personal Details:

Name of the student: _____
(in Block letters): _____
Mobile : _____
Email: _____

Participant's Institute's Details:

Name: _____
Reg.No.: _____
Telephone No. _____
Department: _____

Payment Details:

Cash/ Cheque/ Draft (please specify): _____
Cheque/ Draft details (if applicable): _____

Signature of Candidate with date

PRINCIPAL

Vignan Institute of Technology and Science
Bhongir, Yadadri Dist-508 284

Signature of College Official with date



vits ece <ecevgnt89@gmail.com>

Fwd: WORKSHOP ON MATLAB

1 message

vits ece <ecevgnt89@gmail.com>
To: harikrishna78@gmail.com

3 May 2018 at 15:38

Respected Mam / Sir,

We are one of the 8 well reputed engineering institutions under Vignan group of institutions, situated in Deshmukhi about 6km from Ramoji film city. We have B.Tech and M.Tech courses in many branches of engineering, including Electronics & communication engineering. We have around 500 students including Bachelors and Masters. We would like our students to get exposed to industrial environment and more importantly industry practices, which would help them, upon their entry into any industry, to get set in the industry environment faster and deliver their jobs efficiently.

I am Pleased to invite you as a Visiting Faculty to deliver a series of Lecture in our esteemed organization for the academic year 2017-2018 in your specialized area. This would be a great platform for our students to learn from your experiences, and can enhance their knowledge.

**PRINCIPAL**

Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284

5/10/2018

Gmail - Thank you for invitation.



vits ece <ecevgnt89@gmail.com>

Thank you for invitation.

1 message

harikrishna78@gmail.com <harikrishna78@gmail.com>

4 May 2018 at 10:11

To: vits ece <ecevgnt89@gmail.com>

Hello Sir,

Thank you for invitation.

Required audio-visual equipment : computer/laptop, Matlab Installation Systems, Projector, Wi-Fi is required.

Thank you for giving me such a great opportunity.

Regards,

**Prof. M. Harikrishna, Professor,
Dept of ECE, BITS, Kurnool.**

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Deshmukhi, Bhongir Yadadri Dist-508 284





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Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

Affiliated to JNTU Hyderabad, Programmes Accredited by NBA, Approved by AICTE, New Delhi

EAMCET CODE : VGNT



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE: DESHMUKHI DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

MATLAB Workshop attendance sheet

S.No.	Name of the staff	Department	04/06/2018	05/06/2018	06/06/2018	07/06/2018	08/06/2018
1	Mr. Ramesh	ECE	R	R	R	R	R
2	Mrs.Narmada	ECE	N	N	N	N	N
3	Mrs.CH.Anitha	EEE	A	A	A	A	A
4	Mr.M.Venkateshwarlu	EEE	V	V	V	V	V
5	Mr. V. Durga Prasad	EIE	P	P	P	P	P
6	Mr. V. Hanku	EIE	H	H	H	H	H
7	Mr. K. Vittu	ECE	S	S	S	S	S
8	A. Sushmitha	ECE/VITA	S	S	S	S	S
9	E. Bhargavi	EEE/BRIL	B	B	B	B	B



[Handwritten Signature]

PRINCIPAL

Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284

Co-Ordinator

Mr. V. Prakasam

INSTITUTE VISION AND MISSION

Vision:

To evolve into a center of excellence in Science & Technology through creative and innovative practices in teaching-learning, promoting academic achievement & research excellence to produce internationally accepted competitive and world class professionals who are psychologically strong and emotionally balanced imbued with social consciousness and ethical values.

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- To provide leadership and research aptitude among students of electronics and Communication Engineering.
- To inculcate the exploration skills among the students for the contribution of economic and technological development in national and as well as global arena.

VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Department of Electronics and Communication Engineering



REGISTRATION FORM

WORKSHOP

ON

MATLAB[4th-8th, June, 2018]

Participant's personal Details:

Name of the student:
(in Block letters): K. RAMESH
Mobile : 8790796068
Email: rk@gmail.com

Participant's Institute's Details:

Name: VITS
Reg.No.:
Telephone No.:
Department : ECE

Payment Details:

Cash/ Cheque/ Draft (please specify):
Cheque/ Draft details (if applicable) :

Signature of Candidate with date

PRINCIPAL
Vignan Institute of Technology and Science
Deshmukhi, Bhongir, Yadadri Dist-508 284.



Signature of College Official with date

PRINCIPAL
Vignan Institute of Technology and Science
Deshmukhi, Bhongir, Yadadri Dist-508 284.



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Vignan Hills, Deshmukhi(V), Pochampally(M), near Ramoji Film City Nalgonda(dist)-508284



Certificate of Participation

This is to certify that Mr. Ramesh Designation Non-Teaching

from *Department of Electronics & Communication Engineering of Vignan Institute of Technology & Science* has participated in Workshop on "MATLAB" organized by *Department of Electronics & Communication Engineering* from 4th-8th, June, 2018.

Coordinator



PRINCIPAL

Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadaeri Dist-508 284

Principal

Head of the Department

VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE
Department of Electronics and Communication Engineering

WORKSHOP/FDP FEEDBACK

Name of the Faculty: **K. RAMESH**

Date: **08/06/2018**

Name of the Workshop/FDP: **WORKSHOP ON MATLAB**

College Name: **VITS**

Please agree/disagree with the following statements:

	Excellent	Good	Average	Poor	Remarks
The workshop facilities were comfortable and appropriate.	√				
Contents of the modules		√			
Aspects of program to bridge the gap between theory and application		√			
The length of the workshop was right.		√			
The workshop materials provided were appropriate and helpful	√				
Time management	√				
The instructor was knowledgeable in the subject area	√				
The hands-on exercises supported the lecture and discussion	√				
Overall quality of program	√				



PRINCIPAL

Vignan Institute of Technology and Science
 Deshmukhi, Bhongir Yadaari Dist-508 204

(Handwritten signature in blue ink)

Signature of the Faculty

(Handwritten signature in blue ink)

INSTITUTE VISION AND MISSION

Vision:

To evolve into a center of excellence in Science & Technology through creative and innovative practices in teaching-learning, promoting academic achievement & research excellence to produce internationally accepted competitive and world class professionals who are psychologically strong and emotionally balanced imbued with social consciousness and ethical values.

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VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Department of Electronics and Communication Engineering



REGISTRATION FORM

WORKSHOP

ON

MATLAB[4th-8th, June, 2018]

Participant's personal Details:

Name of the student:

(in Block letters): P. NARMADA

Mobile : 9397381933

Email: narmada123@gmail.com

Participant's Institute's Details:

Name: VITS

Reg.No.:.....

Telephone No.....

Department: ECE

Payment Details:

Cash/ Cheque/ Draft (please specify):

Cheque/ Draft details (if applicable)

Signature of Candidate with date

P. Narmada
2018

Signature of College Official with date

P. Narmada
2018

MOTTO:

This VIT's motto is to become "Excellence through Dedication".

OUR PHILOSOPHY:

To provide high quality engineering education, VITS recognizes that good faculty, library resources and laboratory and infrastructure are the essential components, and therefore, it is making all efforts to provide these resources.

ORGANIZING COMMITTEE

- | | | |
|---|----------------------|-------------|
| 1 | Dr. N. Dinesh Kumar, | Chairman |
| | HOD | |
| 2 | Mr. VijaykumarUrkude | Coordinator |
| 3 | Mr. V. Prakasam | Coordinator |
| 4 | Dr. J. V Rao | Member |

Contact Details:

Mr. V. Prakasam,

Mobile No. 9949140147



PRINCIPAL

Vignan Institute of Technology and Science

Chittoor, Chongir Yadadri Dist-508 284

Vignan Institute of Technology and Science
Chittoor, Chongir Yadadri Dist-508 284



VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Vignan Hills, Deshmukhi(V), Pochampally(M), near Ramoji Film City Nalgonda(dist)-508284



Certificate of Participation

This is to certify that Mrs. Narmada Designation Non-Teaching

from *Department of Electronics & Communication Engineering of Vignan Institute of Technology & Science* has participated in Workshop on "MATLAB" organized by *Department of Electronics & Communication Engineering* from 4th-8th, June, 2018.

Coordinator



PRINCIPAL

Vignan Institute of Technology and Science
Deshmukhi, Bhengir Yadatri Dist-508 284

Principal

Head of the Department

VIGNAN INSTITUTE OF TECHNOLOGY & SCIENCE

Department of Electronics and Communication Engineering

WORKSHOP/FDP FEEDBACK

Name of the Faculty: P. NARMADA

Date: 08/06/2018

Name of the Workshop/FDP: MATLAB

College Name: VITS

Please agree/disagree with the following statements:

	Excellent	Good	Average	Poor	Remarks
The workshop facilities were comfortable and appropriate.	√				
Contents of the modules	√				
Aspects of program to bridge the gap between theory and application	√				
The length of the workshop was right.	√				
The workshop materials provided were appropriate and helpful		√			
Time management			√		
The instructor was knowledgeable in the subject area	√				
The hands-on exercises supported the lecture and discussion	√				
Overall quality of program		√			



P. Narmada

PRINCIPAL

Vignan Institute of Technology and Science
Deshmukhi, Bhongir, Yadadri Dist-508 284

P. Narmada
Signature of the Faculty



VIGNAN INSTITUTE OF TECHNOLOGY AND SCIENCE

Near Ramoji Film City, Deshmukhi (V), Yadadri Bhuvanagiri Dist, Telangana - 508 284.

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EAMCET CODE : VGNT



WORKSHOP REPORT

On

MATLAB

This report provides a brief summary of the Workshop organized by department of Electronics and Communication Engineering and VITS. The Workshop is on “**MATLAB**”. It is 5 days Workshop from date **4th–8th, June, 2018**, from 9 am to 5 pm in the Seminar Hall, 1st Floor, VITS. The VITS Principal graced the occasion and wished grand success for the program, HOD-ECE.

MATLAB (matrix laboratory) is a multi-paradigm numerical computing environment and proprietary programming language developed by Math Works. MATLAB allows matrix manipulations, plotting of functions and data, implementation of algorithms, creation of user interfaces, and interfacing with programs written in other languages, including C, C++, C#, Java, Fortran and Python.

Although MATLAB is intended primarily for numerical computing, an optional toolbox uses the MuPAD symbolic engine, allowing access to symbolic computing abilities. An additional package, Simulink, adds graphical multi-domain simulation and model-based design for dynamic and embedded systems.

As of 2018, MATLAB has more than 3 million users worldwide. MATLAB users come from various backgrounds of engineering, science, and economics.

**Resource Person: Prof. M. Harikrishna, Professor,
Dept of ECE, BITS, Kurnool.**

Duration: 5 hrs per day

Topics covered:

The following topics we learnt from the workshop.

- Signal Processing Techniques
- Introduction to MATLAB, Hands on using Signal
- Processing Toolbox
- FIR & IIR Filter Design
- Multirate Signal Processing



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EAMCET CODE : VGNT



- Digital Image Processing-Image Enhancement,
- Segmentation, Restoration & Morphological
- Operations
- Advanced DIP
- Signal Processing- Research point of view
- DIP hands on using MATLAB
- Basics of Wireless Communications & of Cellular
- Network
- 3G Overview, OFDM
- LTE, LTE Architecture
- Requirements in 5G, 5G Architecture, Issues in 5G
- Signal Processing Requirements for 5G
- Hands on using Communication Toolbox

Co- Ordinator

Mr. V. Prakasam



PRINCIPAL

Vignan Institute of Technology and Science
Deshmukhi, Bhongir Yadadri Dist-508 284